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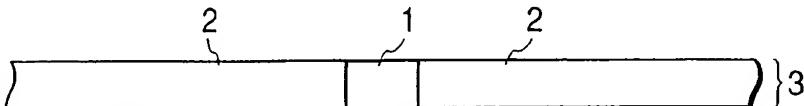
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(54) **Crystalline thin film and process for production thereof, element employing crystalline thin film, circuit employing element, and device employing element or circuit**

(57) A process for producing a crystalline thin film is provided which comprises melting and resolidifying a starting thin film having regions different in the state co-existing continuously. A small region of the starting thin film has a size distribution of number concentration of

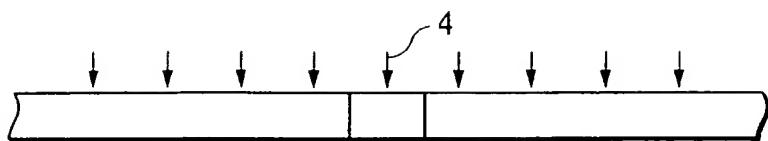
crystal grains or crystalline clusters different from that of the surrounding region. In the process of melting and resolidification, the crystal grain grows preferentially in the one region to control the location of the crystal grain in the crystalline thin film.

**FIG. 1A**

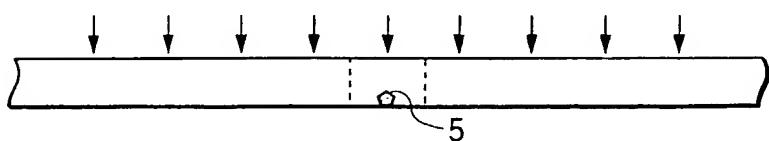


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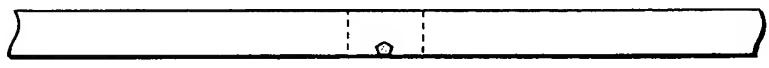
*FIG. 1B*



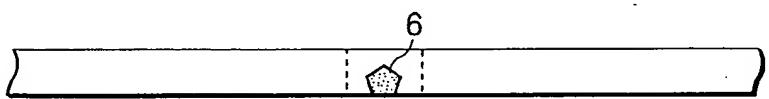
*FIG. 1C*



*FIG. 1D*



*FIG. 1E*



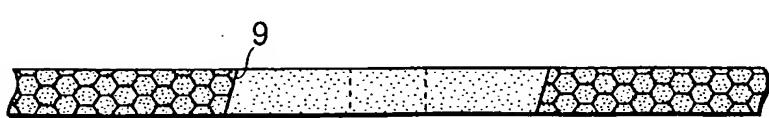
*FIG. 1F*



*FIG. 1G*



*FIG. 1H*



**Description****BACKGROUND OF THE INVENTION**5 **Field of the Invention**

**[0001]** The present invention relates to a crystalline thin film and a process for production thereof, an element employing the crystalline thin film, a circuit employing the element, and a device employing the element or the circuit, which are useful for large-scale integrated circuits requiring high spatial uniformity such as flat panel displays, image 10 sensors, magnetic recording devices, and information/signal processors.

**Related Background Art**

**[0002]** Flat panel displays such as liquid crystal displays have been improved in fineness, display speed, and gradation of image display by monolithic implementation of an image driving circuit to the panel. The simple matrix-driven 15 panels have been replaced with active matrix-driven panels having a switching transistor for each of pixels. At present, ultra-fine full-color liquid crystal displays are provided to be suitable for moving pictures by implementing a shift resistor circuit on the periphery of the same panel for driving the active matrix.

**[0003]** The monolithic implementation including the peripheral driving circuit can be produced at a practical production 20 cost mainly owing to development of the technique for forming a polycrystal silicon thin film having excellent electrical performance on an inexpensive glass substrate: the technique in which amorphous silicon thin film deposited on a glass substrate is melted and resolidified by a short-time pulse projection of light of ultraviolet region such as an excimer laser by keeping the glass substrate at a low temperature. The crystal grain obtained by melting-resolidification has a low defect density in the grain in comparison with crystal grains obtained from the same amorphous silicon thin film 25 by solid-phase crystallization into a polycrystalline thin film. Thereby, the thin film transistor constituted by using this thin film as the active region exhibits a high carrier mobility. Therefore, even with the polycrystalline thin film having an average grain size up to a submicron, an active matrix-driven monolithic circuit can be produced which exhibits sufficient performance in a liquid display having a fineness of 100 ppi or lower in diagonal display size of several inches.

**[0004]** However, it has become clear that the current thin film transistor employing the polycrystalline silicon thin film 30 produced by melting-resolidification is still insufficient in performance for a liquid-crystal display of the next generation having a larger screen or a higher fineness. Furthermore, the aforementioned polycrystalline silicon thin film is insufficient in the performance as the driving circuit element in promising future application fields of plasma displays and electroluminescence displays driven at a higher voltage or larger electric current than the liquid crystal display, or in the application fields of a medical large-screen X-ray image sensor. The polycrystalline silicon thin film, which has 35 average grain size up to submicron, cannot give a high-performance element even with the low defect density in the grain, because of many grain boundaries which hinder charge transfer in the active region of the element having a size of about a micron.

**[0005]** For decreasing the grain boundary density, it is very effective to enlarge the average crystal grain size which 40 is inversely proportional to the grain boundary density. Also, in short-time melting-resolidification of the amorphous silicon thin film, several methods are disclosed to enlarge the average grain size up to a micron or larger. However, by the disclosed methods, while the average grain size is enlarged, the grain size distribution is broadened [e.g., J.S.Im, H.J.Kim, M.O.Thompson, Appl.Phys. Lett. 63, 1969 (1993); H.Kuriyama, T.Honda, S.Ishida, T.Kuwahara, S.Noguchi, S.Kiyama, S.Tsuda, and S.Nakano, Jpn.Appl.Phys. 32, 6190 (1993)]; the spatial location of the grain boundaries is not 45 controlled [e.g., T.Sameshima, Jpn.J.Appl.Phys. 32, L1485 (1993); H.J.Kim and J.S.Im, Appl.Phys.Lett. 68, 1513 (1996)], thereby the increase of the average grain size will increase variation of the grain boundary density among the active regions of the element to result in increase of variation of element performance. Otherwise, the method itself is extremely tricky to be not suitable for practical production [e.g., D.H.Choi, K.Shimizu, O.Sugiura, and M.Matsumura, Jpn.J.Appl.Phys. 31, 4545 (1992); and H.J.Song, and J.S.Im, Appl.Phys.Lett. 68, 3165 (1996)].

**[0006]** Regarding these problems, no idea is given for solving the latter problem, but a methodology has been developed 50 for solving the former problem, in which the location of the grain boundaries and grain size distribution are controlled by controlling the location of formation of crystal grains. This is demonstrated in chemical vapor deposition of polycrystalline thin films and solid-phase crystallization of thin films (see e.g., H.Kumomi and T.Yonehara, Jpn.J. Appl.Phys. 36, 1383 (1997); and H.Kumomi and F.G.Shi, "Handbook of Thin Films Materials", volume 1, chapter 6, "Fundamentals for the formation and structure control of thin films: Nucleation, Growth, Solid-State Transformations" 55 edited by H.S.Nalwa (Academic Press, New York, 2001)).

**[0007]** Several attempts are reported to realize the above idea in formation of crystalline thin film by melting-resolidification. Noguchi [Japanese Patent Application Laid-Open No. 5-102035], and Ikeda [T.Noguchi and Y.Ikeda: Proc. Sony Research Forum, 200 (Sony Corp., Tokyo, 1993)], in melting-resolidification of amorphous silicon thin film by

excimer-laser annealing, prepare openings through a light-intercepting layer provided on a amorphous silicon thin film and projects an excimer laser beam to the amorphous silicon thin film. Thereby, the amorphous silicon at the opening portions is selectively melted and resolidified to form preferential crystallization region in the thin film plane. Toet et al. [D.Toet, P.V.Santos, D.Eitel, and M.Heintze, J.Non-Cryst.Solids 198/200, 887 (1996); D.Toet, B.Koopmans, P.V.Santos, R.B.Bergmann, and B.Richards, Appl.Phys.Lett. 69, 3719 (1996); and D.Toet, B.Koopmans, R.B.Bergmann, B.Richard, P.V.Santos, M.Arbrecht, and J.Krinke, Thin Solid Films 296, 49 (1997)] project an Ar laser beam focused to about 1  $\mu\text{m}$  onto an amorphous silicon thin film to cause melting and resolidification only in the irradiated regions.

5 [0008] However, in the above methods, many crystal grains are formed in the crystallization regions at random positions of the grains in the region, and the number of the crystal grain formed in the regions varied greatly among the regions.

10 [0009] Ishihara and Wilt [R.Ishihara and P.Ch.van der Wilt, J.Appl.Phys. 37, L15 (1998); and P.Ch.van der Wilt and R.Ishihara, Phys.Stat.Sol.(A)166, 619 (1998)] report a method of melting-resolidification of an amorphous silicon thin film by excimer-laser annealing on a single-crystalline silicon substrate covered with an oxide layer, in which method an oxide film is deposited on a single crystalline silicon substrate having hillocks and the deposited oxide film is flattened.

15 [0010] Thereby, the underlying oxide layer is made thinner locally on the top of the hillocks and heat is transferred more rapidly from the thinner portion of the molten silicon film to the substrate to cause rapid cooling. Therefore, the solidification is initiated preferentially from the hillock portions to cause growth of the crystal grain. Ishihara et al. mention the possibility of growing a single crystal grain on the hillocks by optimizing the projected area of the hillocks and the oxide film thickness. However, the possibility has not been confirmed, and the window for the optimum conditions is very 20 small. Moreover this method employs a highly heat-conductive underlying substrate, and is not applicable to a method employing a glass substrate. Furthermore, the fine working of the underlying substrate and the flattening of the oxide film over a large area is not easy and is not practicable.

25 [0011] As described above, in the formation of crystalline thin film by melting-resolidification, the location of the crystal grain formation cannot readily be controlled. However, this method is promising for improving the properties of the thin film, since the thin crystalline film formed by the melting-resolidification method has defects in a low density in the crystal grain in comparison with the films prepared by other thin film formation methods.

30 [0012] The present invention intends to provide a process for producing a general-purpose crystalline thin film applicable to glass substrate or the like by melting-resolidification, in which the location of the crystal grains can precisely be controlled. The present invention intends to provide also a thin film in which the location of the crystal grains can be precisely controlled. The present invention intends further to provide a high-performance element and circuit, and to provide a device by employing the thin film.

## SUMMARY OF THE INVENTION

35 [0013] An embodiment of the present invention is a process for producing a crystalline thin film which comprises melting and resolidifying a starting thin film having regions different in a state thereof from each other coexisting continuously. In particular, in the process for producing a crystalline thin film, the starting thin film has a small region in a state different from a surrounding region, and a prescribed number of crystal grains or crystalline clusters are grown in the small region.

40 [0014] In the above process for producing a crystalline thin film of the present invention, the starting thin film contains a crystal grain or a crystalline cluster, and the state different between the regions is characterized by size distribution of number concentration of crystal grains or crystalline clusters remaining unmelted in the step of melting and resolidifying the starting thin film. In this embodiment, the constitutions below are preferred.

45 [0015] The aforementioned state different between the regions is characterized by a melting point of the bulk portion or surface of the crystal grain or crystalline cluster, or a boundary between adjacent crystal grains or crystalline clusters.

[0016] The aforementioned starting thin film contains an amorphous portion, and the state different between the regions is characterized by a melting point of the bulk portion or surface of the crystal grain or crystalline cluster, a boundary between adjacent crystal grains or crystalline clusters, or the amorphous portion.

50 [0017] The aforementioned starting thin film is an amorphous thin film containing a crystal grain or a crystalline cluster, and the state different between the regions is characterized by size distribution of number concentration of the crystal grains or crystalline clusters contained in the amorphous matrix of the starting thin film.

[0018] The aforementioned starting thin film contains an amorphous region and a polycrystalline region in coexistence, and the state different between the amorphous region and the polycrystalline region is characterized by crystallinity.

55 [0019] The aforementioned starting thin film is an amorphous thin film, and the states different between the regions

are characterized by the height of free-energy barriers to nucleation of crystallite in solid-phase crystallization before the maximum melting moment in the melting and resolidification of the starting thin film, and in particular, the height of free-energy barriers to nucleation of crystallite is controlled by any of an element composition ratio, a contained impurity concentration, a surface adsorbate, and a state of the interface between the starting thin film and the substrate.

5 [0020] In the process for producing a crystalline thin film of the present invention, the state different between the regions is characterized by a height of free-energy barrier to nucleation of crystallite in solidification from a molten phase, and in particular, the height of free-energy barriers to nucleation of crystallite is controlled by any of an element composition ratio, a contained impurity concentration, a surface adsorbate, and a state of the interface between the starting thin film and the substrate.

10 [0021] In the process for producing a crystalline thin film of the present invention, the spatial location of at least a part of crystal grains with a continuous crystal structure is controlled by controlling the spatial location of the regions different in the states in the starting thin film.

[0022] A second embodiment of the present invention is a crystalline thin film, produced by the process for producing a crystalline thin film as described above.

15 [0023] A third embodiment of the present invention is an element employing the crystalline thin film of the present invention; preferably the spatial location of at least a part of the crystal grains with a continuous crystal structure is controlled by controlling the spatial location of the regions different in the states in the starting thin film, and the crystal grains having the controlled location are employed as active regions; and more preferably the active region is formed inside the one single crystal grain in the crystalline thin film.

20 [0024] A fourth embodiment of the present invention is a circuit constituted of the above elements of the present invention. A fifth embodiment of the present invention is a device employing the element or the circuit of the present invention.

#### BRIEF DESCRIPTION OF THE DRAWINGS

25 [0025]

Figs. 1A, 1B, 1C, 1D, 1E, 1F, 1G and 1H show a first basic embodiment of a crystalline thin film and a production process of the present invention.

30 Figs. 2A, 2B, 2C, 2D, 2E, 2F, 2G and 2H show a second basic embodiment of a crystalline thin film and a production process of the present invention.

Figs. 3A, 3B, 3C, 3D, 3E, 3F, 3G and 3H show another embodiment of a crystalline thin film and a production process of the present invention.

35 Figs. 4A, 4B, 4C, 4D, 4E, 4F, 4G and 4H show another embodiment of a crystalline thin film and a production process of the present invention.

Figs. 5A, 5B, 5C, 5D, 5E, 5F, 5G and 5H show another embodiment of a crystalline thin film and a production process of the present invention.

40 Figs. 6A, 6B, 6C, 6D, 6E, 6F, 6G and 6H show another embodiment of a crystalline thin film and a production process of the present invention.

Figs. 7A, 7B, 7C, 7D, 7E, 7F, 7G and 7H show another embodiment of a crystalline thin film and a production process of the present invention.

45 Figs. 8A, 8B, 8C, 8D, 8E, 8F, 8G and 8H show another embodiment of a crystalline thin film and a production process of the present invention.

Figs. 9A, 9B, 9C, 9D, 9E, 9F, 9G and 9H show another embodiment of a crystalline thin film and a production process of the present invention.

50 Fig. 10 shows a first basic embodiment of the element of the present invention.

Fig. 11 shows a second basic embodiment of the element of the present invention.

Fig. 12 shows another embodiment of the element of the present invention.

Fig. 13 shows another embodiment of the element of the present invention.

Fig. 14 shows an embodiment of the circuit of the present invention.

Fig. 15 shows an embodiment of the device of the present invention.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

55 [0026] A first problem to be solved by the present invention is precise control of location of crystal grain formation in melting resolidification of thin films. In conventional methods as described above, the location is controlled by preferential resolidification by causing thermal distribution in the molten thin film. Such a method might be promising if the temperature distribution in the thin film could be precisely controlled spatially with time, since formation and growth of

the crystal grain during and after the melting depend greatly on the temperature of the thin film. However, the conduction of heat in the thin film is considerably or significantly rapid before and after the melting, so that a steep temperature gradient cannot readily be localized within desired limited regions even during a short period of melting-resolidification, as suggested by simulation of two-dimensional thermal analysis [Gupta, Song, and Im, Mat.Res.Soc.Symp.Proc. 397, 465 (1996)].

5 Further, in short-time annealing by short-wavelength light source like excimer laser, the formation of a prescribed temperature distribution throughout the process of crystal grain formation is impossible inherently, since the melting and resolidification varies also in the film thickness direction as is evident from the aforementioned simulation of thermal analysis and the result of the in-situ observation [Hatano, Moon, Lee, and Grigoropoulos, J.Appl.Phys., 87, 36 (2000)]. The inventors of the present invention have noticed that the above facts causes the difficulty in the 10 control of the location of one-crystal grain formation or the control of the number density of formed crystal grains. The inventors of the present invention, after comprehensive investigation, have found a novel method which is completely different from the conventional methods based on the thermal distribution.

15 [0027] The present invention is characterized in that the starting thin film to be treated for melting and resolidification has regions different in state. The present invention is characterized also in that the starting thin film to be treated for melting and resolidification has a small region which is different in a state from a surrounding region, and a prescribed 20 number of crystal grains or crystalline clusters are preferentially grown in the small regions.

[0028] The presence of the regions different in the state in the present invention affects the melting-resolidification. For explaining the effects, firstly a dynamic aspect of the melting-resolidification process is described, which is being 25 elucidated in recent years.

[0029] The formation of the crystalline thin film by melting and resolidification is initiated with the melting by heating of the starting thin film. In the case where the thin film immediately before the melting has an inhomogeneous structure constituted of, for example, a polycrystalline matter and an amorphous matter containing crystalline clusters, the melting point is nonuniform in the thin film depending on the components of the thin film. Generally, the melting point of the bulk region in the crystal grain or the crystalline cluster ( $T_C$ ), the melting point of their surface ( $T_S$ ), and the melting point of the grain boundary between the adjacent grains ( $T_B$ ), and the melting point of the amorphous region ( $T_A$ ) are 30 known to be in the following relation:  $T_C > T_S > T_B > T_A$ . In the case where the thin film immediately before the melting is constituted of such components, the variations of the melting process are classified and summarized as shown in Tables 1 to 4 in relation to the maximum temperature ( $T_M$ ) for the melting, the heating rate ( $dT/dt$ ), the duration time of the maximum temperature, and the constitution of the starting thin film.

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Table 1

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
10						
15	$T_M > T_C$	$\infty$	>0	Entire film melts instantaneously.		
		< $\infty$ Rapid		At temperature exceeding $T_B$ , melting begins at grain boundary, and the melting spreads from surface of crystal grain or crystalline cluster. Finally entire thin film melts.	At temperature exceeding $T_A$ , melting begins at amorphous region. At temperature exceeding $T_S$ , crystal grain or crystalline cluster begins to melt at the surface. Finally entire thin film melts.	At temperature exceeding $T_A$ melting begins locally. Finally entire thin film melts.
20						
25		< $\infty$ Slow		At temperature below $T_A$ , crystal grain or crystalline cluster grows in solid phase and in amorphous region, spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_B$ melting begins at grain boundary, and melting spread from surface of crystal grain. Finally entire thin film melts.	At temperature below $T_A$ , spontaneous nucleation occurs locally at high rate and high density. At temperature exceeding $T_B$ melting begins at grain boundary, and melting spread from surface of crystal grain. Finally entire thin film melt.	
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Table 2

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
10						
15	$T_C > T_M > T_B$	$\infty$	Long	Grain boundary melts instantaneously. Crystal grain or crystalline cluster becomes smaller in size by surface melting.	Amorphous region melts instantaneously. Crystal grain or crystalline cluster becomes smaller in size by surface melting.	Entire thin film melts instantaneously.
20			Short	Grain boundary melts instantaneously. Surface of crystal grain or of crystalline cluster melts, but hardly becomes smaller in size. Polycrystal composed of crystal grains or crystalline clusters	Amorphous region melts instantaneously. Surface of crystal grain or of crystalline cluster melts, but hardly becomes smaller in size. Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
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Table 2 (continued)

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
10						
15	$T_C > T_M > T_B$	$\infty$ Rapid	Long	At temperature exceeding $T_B$ , melting begins at grain boundary. Crystal grain or crystalline cluster becomes smaller in size by surface melting.	At temperature exceeding $T_A$ , melting begins at amorphous region. At temperature exceeding $T_S$ , crystal grain or crystalline cluster becomes smaller in size owing to melting of surface.	At temperature exceeding $T_A$ , melting begins locally. Finally entire thin film melts.
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Table 3

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
10						
15	$T_C > T_M > T_B$	$0 < \infty$ Slow	Long	At temperature exceeding $T_B$ , melting begins at grain boundary. Crystal grain or crystalline cluster becomes smaller in size by surface melting.	At temperature below $T_A$ , crystal grain or crystalline cluster grows in solid phase and in amorphous region, spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_B$ melting begins at grain boundary, and crystal grain becomes smaller in size remarkably.	At temperature below $T_A$ , spontaneous nucleation occurs locally at high rate and high density. At temperature exceeding $T_B$ melting begins at boundary, and crystal grain or crystalline cluster becomes smaller in size remarkably.
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40				polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
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Table 3 (continued)

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
10						
15	$T_C > T_M > T_B$	$0 < \infty$ Slow	Short	At temperature exceeding $T_B$ , melting begins at grain boundary. Crystal grain or crystalline cluster melts at surface, but hardly becomes smaller in size.	At temperature below $T_A$ , crystal grain or crystalline cluster grows in solid phase and in amorphous region, spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_B$ , melting begins at grain boundary, but crystal grain size hardly becomes smaller in size.	At temperature below $T_A$ , spontaneous nucleation occurs locally at high rate and high density. At temperature exceeding $T_B$ , melting begins at grain boundary, but crystal grain or crystalline cluster hardly becomes smaller in size.
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Table 4

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film					
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster			
10	$T_S > T_M > T_A$	$\infty$ $\approx$ Rapid $\approx$ Midium	>0	No melting  At temperature exceeding $T_A$ , amorphous layer only melts.  At temperature below $T_A$ , crystal grain or crystalline cluster grows in solid phase and in amorphous region, spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_A$ , remaining amorphous region only melts.	Amorphous region only melts instantaneously.  At temperature exceeding $T_A$ , entire thin film melts.  At temperature below $T_A$ , spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_A$ , remaining amorphous region only melts.	Entire of thin film melts instantaneously.  At temperature exceeding $T_A$ , entire thin film melts.  At temperature below $T_A$ , spontaneous nucleation occurs at high rate and high density. At temperature exceeding $T_A$ , remaining amorphous region only melts.			
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25				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster			
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Table 4 (continued)

5	Maximum Temperature $T_M$	Heating rate $dT/dt$	Duration of maximum temperature	Constitution of starting thin film		
				Polycrystal composed of crystal grains or crystalline clusters	Amorphous matter containing crystal grains or crystalline clusters	Amorphous matter containing neither crystal grain nor crystalline cluster
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[0030] In annealing by a short-wavelength light source such as an excimer laser, the laser light penetrates to a small depth into the thin film to heat only the portion of the film near the surface, and the underlying portion of the film is heated by heat conduction from the surface portion. Therefore, the transition process in the film depth direction is included in the classification of the dynamic aspect of the melting process shown in Tables 1 to 4. That is, the melting begins near the surface and propagates toward the interface between the film and the underlying substrate. The state of the melting at the respective depth follows Tables 1 to 4.

[0031] Among the melting processes summarized in Tables 1 to 4, the systems in which none of the portion of the thin film is melted are excluded from the consideration in the present invention since such systems are not the objects of the present invention. Therefore, the melting states are roughly classified into the states of (1) complete melting states in which the entire thin film is in a molten phase, and (2) incomplete melting states in which crystalline solid regions are dispersed in the molten phase, regardless of the variations of the maximum temperature, the heating rate, the maximum temperature duration time, and the constitution of the starting thin film. Next, the resolidification process will be discussed, where the thin film in either of the above two states of melting is cooled after completion of the heating.

[0032] For initiating the solidification of a thin film in a complete melting state, a crystalline nucleus should be spontaneously formed in the molten phase. According to a classical nucleation theory, the number of crystalline nuclei produced in a unit time in a unit volume in the molten phase, namely the nucleation rate ( $J$ ) is a function of the free-energy barrier ( $W^*$ ) to the crystalline nucleus formation and the absolute temperature ( $T$ ) of the system as  $J \propto \exp(-W^*/kT)$ . The free-energy barrier ( $W^*$ ) to the crystallite nucleation is a function of the melting point ( $T_C$ ) of the crystal bulk and the supercooling ( $\Delta T = T - T_C$ ) as  $W^* \propto T_C/\Delta T$ , whereby  $J$  increases multiple-exponentially with decrease of  $T$ . Therefore, the nucleation does not occur immediately after the temperature becomes lower than the melting point  $T_C$ , keeping the molten state. After a certain time when the temperature has become sufficiently lower, the nucleation occurs spontaneously and explosively to cause rapid solidification. The supercooling to cause observable spontaneous nucleation and the final crystal grain concentration depend on the cooling rate  $dT/dt$  and the free-energy barrier  $W$ . to the crystalline nucleus formation. The latter depends not only on the supercooling but also on the state of the starting material.

[0033] On the other hand, in a thin film in an incomplete melting state, the liquid-phase epitaxial growth begins simultaneously with the start of the cooling, from seed crystallites which have been dispersed crystal grains or crystalline clusters remaining unmelted in the molten matrix. Therefore, the unmelted crystal grains or crystalline clusters remaining in the molten matrix grow precedently, and when the supercooling has become significant, the fine crystal grains are formed spontaneously and quickly only in the molten region. When the thin film is cooled gradually, or when the concentration of the crystal grains or crystalline clusters remaining in the molten matrix is sufficiently high, or the size thereof is sufficiently large, the entire thin film can be solidified only by the growth of the remaining crystal grains or crystalline clusters. On the other hand, when the thin film is cooled quickly, or when the concentration of the crystal grains or the crystalline clusters remaining in the molten matrix is significantly low, or the size thereof is significantly small, most of the regions in the thin film can be solidified with fine crystal grains by increasing the supercooling to cause spontaneous explosive nucleation before significant growth of the remaining crystal grains or crystalline clusters. Accordingly, the final number concentration of the crystal grains and the size distribution thereof depend on the cooling rate  $dT/dt$  and size distribution of the remaining crystal grains or the crystalline clusters remaining in the molten matrix. Further, the latter depends, as shown in Tables 1 to 4, on (1) the temperature conditions of the thin film such as the maximum temperature  $T_M$ , the heating rate  $dT/dt$ , and the maximum temperature duration; and (2) such a condition of the starting thin film as the following (2-1) and (2-2): (2-1) the structure associated with the crystallinity of the starting thin film which is described by the size distribution of number concentration of the crystal grains or the crystalline clusters, and (2-2) the thermodynamic properties such as the free-energy barrier to the crystallite nucleation in solid-phase crystallization before the melting of the starting thin film, the melting point  $T_C$  of bulk region in the crystal grains and crystalline clusters, the melting point  $T_S$  of the surface thereof, the melting point  $T_B$  of the grain boundary between the adjacent crystal grains, and the melting point  $T_A$  of any amorphous region if it is present.

[0034] In the present invention, the wording "the size distribution of number concentration of crystal grains and crystalline clusters" means a physical quantity defined by a number of g-sized crystal grains or crystalline clusters in a unit volume,  $f(g)$ , as a function of g.

[0035] Therefore, two "size distributions of number concentration of crystal grains and crystalline clusters" are equivalent to each other only when the  $f(g)$  values coincide for any "g" in the entire size space region ( $g>0$ ). Otherwise, they are different.

[0036] From the above consideration, it is generally concluded that the gain structure of the polycrystalline thin film derived by melting-resolidification depends on the heating temperature conditions for melting and cooling for resolidification, and the states of the starting thin film including the structure and thermodynamic properties, regardless of the state of complete melting or incomplete melting. From the standpoint of control of the spatial location of crystal grain formation, the above temperature condition control has been attempted so far, but this control is not easy as mentioned above. After comprehensive consideration, the inventors of the present invention have foreseen reasonably the possibility of control of the location of crystal grain formation by controlling the spatial state of the starting thin film. That is, the means for solving the problem in the present invention is to control the state of the starting thin film spatially. The effects thereof are described by reference to embodiments

[0037] Figs. 1A to 1H and 2A to 2H illustrate basic embodiments of the crystalline thin film and the process for production thereof of the present invention. Figs. 3A to 3H through 9A to 9H illustrate developed embodiments. The drawings are schematic sectional views of a fraction of the thin film cut in a direction perpendicular to the surface or the interface. The thin film may be in contact with another layer or layers on its upper face or lower face in the present invention. In Figs. 1A to 1H through 9A to 9H, only the thin film is illustrated without showing the additional layer. In Figs. 1A to 1H through 9A to 9H, the numerals indicate the parts as follows: 1, a small region; 2, a surrounding region; 3, a starting thin film; 4, application of energy for melting-resolidification; 5, a crystal grain or a crystalline cluster; 6, a crystal grain; 7, a molten region; 8, a random crystalline cluster; and 9, a grain boundary.

[0038] In order to control the state of the starting thin film spatially, in the starting thin film, small regions are formed which are in a state different from the surrounding regions, and in the small regions, a prescribed number of crystal grains or crystalline clusters are allowed to remain unmelted at the maximal melting, or crystallite nucleation is characterized preferentially therein. As shown in Fig. 1A or Fig. 2A, small regions 1 are formed in starting thin film 3 to coexist with surrounding regions 2 in contact with each other, and thereto energy 4 is applied to heat and melt starting thin film 3 as shown in Fig. 1B or Fig. 2B. Small regions 1 and surrounding regions 2 are different in the state of the thin film as shown below.

[0039] Small regions 1 and surrounding regions 2 are made different such that, between the final stage of the melting (Fig. 1C) and the stage after the end of energy application (Fig. 1D), one or more of a prescribed number of crystal grains or crystalline clusters remain unmelted in small regions 1, and the crystal grains or the crystalline clusters in surrounding regions remain in a concentration much lower than that in small regions 1; or surrounding regions 2 are completely melted without the crystal grains or the crystalline clusters remaining unmelted. Otherwise, small regions 1 and surrounding regions 2 are made different such that both of small region 1 and surrounding region 2 are completely melted in the final stage of the melting (Fig. 2C), but nucleation occurs preferentially in small regions 1 for formation

of a crystal grain or a crystalline cluster 5 in the cooling-solidification process (Fig. 2D) because the free-energy barrier to crystallite nucleation in small region 1 is lower than that in surrounding region 2.

[0040] After the heating-melting of the starting thin film having small region 1 and surrounding region 2 different in the state, during the resolidification, the crystal growth proceeds from the crystal grain or the crystalline cluster 5 which works as the seed crystal to solidify the adjacent molten-state thin film, and to form crystal grain 6 (Figs. 1E and 2E).

[0041] Thereafter, crystal grain 6 grows beyond small region 1 by solidifying successively adjacent surrounding region 2 (Fig. 1F and 2F). However, the growth of crystal grain 6 is not endless. In molten region 7 where the solidification is not characterized by crystal grain 6, at a certain supercooling, crystal grains or crystalline clusters 8 are formed spontaneously and explosively at random (Figs. 1G and 2G). Crystal grain 6 having grown into surrounding region 2 impinges upon the crystal grains or crystalline clusters which are not location-controlled, and grain boundaries 9 are formed between them (Figs. 1H and 2H). Consequently, a crystalline thin film is formed by melting-resolidification with crystal grain 6 at the controlled location in small region 1.

[0042] In the case where small region 1 is incompletely melted as shown in Figs. 1A to 1H, the number of crystal grains or crystalline clusters 5 to remain in small region 1 in the incomplete melting can be controlled precisely by the size distribution of number concentration of crystal grains or crystalline clusters contained before the melting, the volume of small region 1, or the conditions of application of energy 4 for the heating-melting.

[0043] In the case where small region 1 and surrounding region 2 are both completely melted, the number of crystal grains or crystalline clusters 5 formed preferentially in small region 1 depends only on the free-energy barrier to nucleation of crystallite W\*, which can be controlled precisely by the state of small region 1 and the temperature at solidification.

[0044] In the both cases of the incomplete melting of small region 1 and the complete melting of small region 1, the number of crystal grain 6 in small region 1 is not limited to one, but two or more may be grown as shown in Figs. 3A to 3H for the incomplete melting of small region 1.

[0045] In Figs. 1A to 1H, Figs. 2A to 2H, and Figs. 3A to 3H, small region 1 for defining the location of formation of crystal grain 6 is provided as a single domain surrounded by surrounding region 2. However, the embodiment is not limited thereto, and small regions 1 may be provided in plurality, discontinuously or scatteredly in the present invention.

[0046] Figs. 4A to 4H, Figs. 5A to 5H and Figs. 6A to 6H show other embodiments in which small region 1 is incompletely melted, corresponding to embodiment of Figs. 1A to 1H. In the case where adjacent small regions 1 are far apart from each other (Fig. 4A), fine crystal grains or crystalline clusters 8 are formed spontaneously and explosively at random between crystal grains formed at the positions of the respective small regions 1 (Fig. 4H). However, in the case where the distance between the adjacent small regions 1 is short enough (Fig. 5A), crystal grains having grown therefrom come to collide together at around the midpoint between them to form grain boundary 9 before the spontaneous-explosive formation of crystal grains or crystalline clusters 8 (Fig. 5H).

[0047] With small regions 1 arranged throughout the thin film with the maximum distance less than the aforementioned distance (Fig. 6A), the entire thin film can be constituted of locationally controlled crystal grains 6 (Fig. 6H). With small regions 1 arranged at regular intervals, the thin film can be formed with locationally controlled crystal grains 6 in nearly the same sizes.

[0048] In examples shown in Figs. 4A to 4H, Figs. 5A to 5H, and Fig. 6A to 6H, small regions 1 are incompletely melted. However, a similar process can be employed in the cases where small regions 1 are completely melted as shown in Figs. 2A to 2H.

[0049] In the embodiments shown in Figs. 1A to 1H through Figs. 6A to 6H, small regions 1 and surrounding regions 2 are arranged two-dimensionally in the plane direction of starting thin film 3. In the present invention, a three-dimensional configuration may be employed in which the regions are arranged also in the thickness direction of starting thin film 3. For example, as shown in Figs. 7A to 7H, small regions 1 are formed in the half thickness portion of the thin film, and are surrounded from the upper side and peripheral side by surrounding regions 2, whereby similar melting-resolidification can be conducted. In Figs. 7A to 7H, small regions 1 arranged at short intervals as in Figs. 6A to 6H are melted incompletely. Three-dimensional arrangements of small regions 1 are also possible in the constitutions as shown in Figs. 1A to 1H to Figs. 5A to 5H or in the cases where small regions 1 are completely melted. Furthermore, the three dimensional configuration of small regions 1 is not limited to those shown in Figs. 7A to 7H but includes various variations.

[0050] In the embodiments shown in Figs. 1A to 1H through Figs. 7A to 7H, the spreading area of starting thin film 3 in the plane direction is far greater than the size of small regions 1 or crystal grains 6, or than the intervals between small regions 1. However, the area and the size or the intervals may be made comparable in size. For example, in Figs. 8A to 8H, with starting thin film 3 in a size shown in the drawing, when small region 1 is formed to be surrounded by surrounding regions 2 (Fig. 8A) and melting-resolidification is conducted to cause incomplete melting of small region 1, the resulting resolidified thin film has crystal grain 6 surrounded by crystal grains or crystalline clusters 8 formed by spontaneous random nucleation not controlled locationally (Fig. 8H). In Figs. 9A to 9H, with starting thin film 3 smaller in size in plane direction (Fig. 9A), crystal grain 6 grows throughout the entire of starting thin film 3, before explosive

occurrence of spontaneous nucleation of crystal grains or crystalline clusters 8 without location control, to form a resolidified thin film constituted only of crystal grain 6 (Fig. 9H). The melting-resolidification shown in Figs. 8A to 8H and Figs. 9A to 9H can also be conducted in the case where small region 1 melts completely as shown in Figs. 2A to 2H, in the case where plural crystal grains 6 grow in small region 1 as shown in Figs. 3A to 3H, in the case where starting thin film 3 contains plural small regions 1 as shown in Figs. 4A to 4H to Figs. 6A to 6H, or in the case where small regions 1 is arranged three-dimensionally as shown in Figs. 7A to 7H. Starting thin films 3 shown in Figs. 8A to 8H and Figs. 9A to 9H may be provided in plurality scatteredly on a substrate not shown in the drawings.

5 [0051] In the embodiments shown in Figs. 1A to 1H through Figs. 9A to 9H, starting thin film 3 has two kinds of regions different in the state, namely small region 1 and surrounding region 2, but three or more kinds of regions different in the state may be provided. For example, small regions 1 as shown in Figs. 1A to 1H in which respectively single crystal grain 6 is preferentially formed, and another kind of small regions 1 as shown in Figs. 3A to 3H in which plural crystal grains 6 are formed may coexist on one starting thin film 3. Otherwise, plural small regions having different structures two-dimensionally or three-dimensionally may coexist on a starting thin film 3.

10 [0052] A basic embodiment for forming small regions 1 and surrounding regions 2 in coexistence in starting thin film 3 in the melting-resolidification process is explained below.

15 [0053] In the case where small region 1 is incompletely melted, small region 1 and surrounding region 2 are made different from each other such that, in the final stage of melting of the thin film, one or a certain number of crystal grains or crystalline clusters 5 remain unmelted in small region 1, whereas, in surrounding region 2, crystal grains or crystalline clusters remain unmelted at a concentration much lower than that in small region 1 or do not remain to result in complete melting. As described before, the size distribution of number concentration of unmelted crystal grains or crystalline clusters depends on the structure relating to the crystallinity of the starting thin film defined by the size distribution of number concentration of the unmelted crystal grains or crystalline clusters contained in the starting film; and on thermodynamic properties such as the free-energy barrier to nucleation of crystallite, the melting point of the bulk region in the crystal grain or crystalline cluster, the melting point of the surface thereof, and the melting point of grain boundary between the adjacent crystal grains, and the melting point of amorphous region in the case where the starting thin film contains an amorphous region. Therefore, for the present purpose, any of the above factors is made different between small region 1 and surrounding region 2.

20 [0054] Unmelted crystal grains or crystalline clusters can be allowed to remain, for example, when the size distribution of number concentration of crystal grains or crystalline clusters shifts to a larger size range than that of the surrounding region 2 (grains or clusters with larger size are larger in number), or has a larger concentration (number of grains or clusters is larger), and the starting thin film is melted without undergoing solid-phase crystallization. Even when the starting thin film is melted undergoing solid-phase crystallization, the difference in the initial size distribution of number concentration of crystal grains or crystalline clusters is reflected directly to the size distribution after the solid-phase crystallization, whereby unmelted crystal grains or crystalline clusters can be made remain in the melt selectively in small region 1. In the case where the solid-phase crystallization before melting of the starting thin film is not characterized by the growth of the grains larger than the critical size for crystallite nucleation but is governed mainly by the crystallite nucleation process, the height of the free-energy barrier to nucleation of crystallite in the small region 1 is made different from that in surrounding region 2. The height of the free-energy barrier to crystallite nucleation in the solid-phase crystallization depends on the elemental composition ratio of the thin film, the concentration of the impurity contained, the adsorbate on the surface, and the state of the interface between the thin film and the substrate. Therefore, by designing any of the above factors to be different between the regions, small region 1 and surrounding region 2 can be provided in one and the same thin film. Thereby, immediately before the melting, the size distribution of number concentration of crystal grains or crystalline clusters in small region 1 belongs to a larger size range than that in surrounding region 2, whereby the unmelted crystal grain or crystal cluster is allowed to remain selectively in small region 1. Otherwise, by designing the melting point of any of the bulk region in the crystal grain or crystalline clusters, the surface thereof, and grain boundary between adjacent crystal grains, in small region 1 to be higher than that of surrounding region 2, an unmelted crystal grain or crystalline cluster can be allowed to remain selectively in small region 1 in the final stage of the melting process.

25 [0055] In the other case where small region 1 is completely melted, the state of the starting thin film is made different between small region 1 and surrounding region 2 by designing the free-energy barrier to nucleation of crystallite from molten phase W to be lower in small region 1 than in surrounding region 2. Thereby, in the cooling-solidification process after complete melting of small region 1 and surrounding region 2, nuclei of the crystal grains or crystalline clusters are preferentially formed in small region 1 while the supercooling is not significant. As described before, the concentration of the crystal grains or crystalline clusters formed by spontaneous nucleation depends only on the free-energy barrier W to nucleation of crystallite, and the value of W depends on the state of small region 1 such as the elemental composition ratio of the starting thin film, the concentration of the impurity contained, the adsorbate on the surface, and the state of the interface between the thin film and the substrate. Therefore, small region 1 and surrounding region 2 are made different in any of the above factors.

[0056] Figs. 10 to 13 show embodiments of the element of the present invention which employ the crystalline thin film formed by the above described melting-resolidification process. Figs. 10 to 13 show partial cross section of the thin film cut in a direction perpendicular to the surface or the interface thereof similarly as Figs. 1A to 1H through Figs. 9A to 9H. In Figs. 10 to 13, crystal grain or crystalline cluster 8 formed by random spontaneous nucleation as shown in Figs. 1A to 1H through Figs. 5A to 5H or Figs. 8A to 8H is omitted and only the locationally controlled crystal grain 6 is shown, and substrate 100 for supporting the thin film is shown as necessary.

[0057] In the crystalline thin film of the present invention, the position of crystal grain 6 is determined by the position of small region 1, and the size thereof is predetermined. Therefore, in forming the element employing crystal grain as the active region, the active region employing crystal grain 6 is readily related to the position of crystal grain 6. For example as shown in Fig. 10, active small region 10 of the element can be confined to the inside of crystal grain 6. Thereby the performance of the element is improved and variation thereof among the elements is decreased since no grain boundary is included in active small region 10 of the element.

[0058] In the crystalline thin film prepared by the process for providing plural small regions 1 as shown in Figs. 5A to 5H through 7A to 7H, active small region 10 can be provided in the region containing desired number of crystal grains 6 as shown in Fig. 11. In this case also, the number of crystal grains 6 or the density of the grain boundary included in active small region 10 of the element can be controlled, since the positions of the small regions 1 are predetermined. Thereby, the variation between the elements can be reduced.

[0059] Active small region 10 of the element shown in Figs. 10 or 11 is formed by providing suitable input-output terminals onto crystal grain 6. According to the kind of the element, the input-output terminals are in any positions of the upper face, lower face, and peripheral face of the thin film comprising crystal grain 6. The input-output terminal may be in contact with crystal grain 6 or apart therefrom depending on the type of the signals inputted to or outputted from the active region of the element.

[0060] Figs. 12 and 13 show examples of MOS type thin transistors (TFT) provided on a crystalline thin film constituted of a semiconductor material. In the drawings, the numerals indicate the parts as follows: 11, a gate insulation film; 12, a gate electrode; 13, a source electrode; 14, a drain electrode; and 100, a substrate.

[0061] The element shown in Fig. 12 is a MOS type TFT employing single crystal grain 6 formed isolatedly on substrate 100 through the process shown in Figs. 9A to 9H. On the surface of crystal grain 6, there are provided gate electrode 12 with interposition of gate insulation film 11, source electrode 13, and drain electrode 14. In the regions of crystal grain 6 under the electrodes, are formed respectively a channel region, a source region, and a drain region by controlling the conductivity type. This element can give high performance since the entire element is formed inside single crystal grain 6 without grain boundary. By forming plural elements on one and the same substrate 100 as shown in Fig. 12, the variation of the performances among the elements can be decreased. In the example shown in Fig. 13, a crystalline thin film is formed by a process shown in Figs. 1A to 1H or Figs. 2A to 2H, a gate region only is formed on the portion of single crystal grain 6, and a source region and a drain region are formed on a portion of the thin film adjacent to crystal grain 6 and containing crystal grains or crystalline clusters 8 not locationally controlled. In such a MOS type TFT, the element performance depends mainly on the electric charge transfer in channel region as the active region. Therefore, the plural elements shown in Fig. 13 give high performance with less performance variation among the elements.

[0062] In the example shown in Figs. 12 and 13, the active region of the element does not contain grain boundary. However, the crystalline thin film having grain boundary 9 controlled locationally, that are obtained by the process of any of Fig. 3A to 3H, Figs. 5A to 5H to Figs. 7A to 7H, can contain crystalline grain boundary 9 in the active region of the element. The element having grain boundary 9 in the active region may be inferior in the absolute value of the performance, but the variation among the elements can be decreased by controlling the density of the grain boundaries.

[0063] Fig. 14 shows an example of the circuit of the present invention which is constituted by employing the elements of the present invention as shown above. In the drawing, the numerals indicate the parts as follows: 15, a gate wiring electrode of a first TFT; 16, a gate wiring electrode of a second TFT; and 17, an insulating layer. This circuit has, as a part, two MOS-type TFT comprising respectively single crystal grain 6 grown from small region 1 as the growth center as shown in Fig. 12 on one substrate 100. Drain electrode 14 of the first TFT controlled by gate electrode 12 is connected through a wiring to gate electrode 16 of the second TFT, and the electrodes and the wiring are insulated from each other by insulating layer 17. Thus, the second TFT controlled by gate electrode 16 is controlled by the drain voltage of the first TFT. In such a circuit, the element performances of the first TFT and the second TFT should be controlled precisely. The circuit of this example not containing grain boundary in the active region will satisfy the above conditions.

[0064] Fig. 15 shows an example of the image displaying device of the present invention which comprises the circuit of the present invention. In the drawing, the numerals indicate the parts as follows: 18, an electrode; 19, a light-emitting layer or a light-transmission control layer; and 20, an upper electrode. This device comprises, as a part, two connected TFTs like that shown in Fig. 14. The drain electrode of the second TFT is connected to electrode 18 at the upper position of the element. On electrode 18, light-emitting layer or light-transmission control layer 19 is provided, and further thereon upper electrode 20 is provided. The voltage applied to light-emitting layer or light-transmission control layer 19 or the

current introduced thereto by electrode 18 and upper electrode 20 depends on the drain voltage or current of the second TFT controlled by the first TFT drain voltage. The light emission intensity or light transmittance of the light-emitting layer or light transmission control layer 19 is controlled by the voltage applied thereto or electric current introduced thereto. The image displaying device of this embodiment employs the above element as a display unit for one pixel, and is constituted of such elements arranged in a grid pattern. For achieving uniform light intensity and time response, the variation of the performance between the pixels should be minimized. The above device employing the circuit comprising elements containing no grain boundary in the active region satisfies the above conditions.

## 5 [Example 1]

10 [0065] A crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 1 of the present invention.

[0066] An amorphous silicon thin film containing crystalline silicon clusters was formed in a thickness of 50 nm by vapor deposition on a substrate composed of silicon oxide and other components and having an amorphous surface.

15 This amorphous silicon thin film containing crystalline silicon clusters was irradiated with an energy beam from the surface side excluding locally a small region of 1  $\mu\text{m}$  diameter to partially amorphize the crystalline silicon clusters in the irradiated region of the thin film. The resulting film was employed as the starting thin film.

[0067] This starting thin film was irradiated with a KrF excimer laser beam at an energy density of about 200 mJ/cm<sup>2</sup> for 30 nsec for melting and resolidification to obtain a crystalline thin film.

20 [0068] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that single crystal grain had grown around the above small region of about 1  $\mu\text{m}$  diameter as the center to a size of about 2  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

[0069] In the starting thin film of this Example, in the small region of about 1  $\mu\text{m}$  diameter which was not irradiated with the energy beam, the average value of the size distribution of number concentration of crystalline clusters, and the concentration thereof are higher than in the other regions. This difference in the size distribution of number concentration of crystalline clusters makes difference in the states among the continuously placed regions to form "small region 1" of about 1  $\mu\text{m}$  diameter as shown in Figs. 1A to 1H. By in-situ observation of the melting-resolidification process of the energy beam-irradiated thin film and of a non-irradiated thin film

25 under the aforementioned melting-resolidification conditions, the former was confirmed to be completely melted, whereas the latter was confirmed to be incompletely melted.

[0070] As described above, in this Example, the starting thin film was an amorphous thin film containing crystalline clusters, and the size distribution of number concentration of crystalline clusters was different between the regions in the amorphous base material of the starting thin film. Thereby, in the melting-resolidification of the starting thin film,

30 the size distribution of number concentration of the remaining unmelted crystalline clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[0071] As described above, in this Example, the starting thin film was an amorphous thin film containing crystalline clusters, and the size distribution of number concentration of crystalline clusters was different between the regions in the amorphous base material of the starting thin film. Thereby, in the melting-resolidification of the starting thin film,

35 the size distribution of number concentration of the remaining unmelted crystalline clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

## [Example 2]

40 [0072] Another crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 2 of the present invention.

[0073] An amorphous silicon thin film containing crystalline silicon clusters was formed in a thickness of 50 nm by vapor deposition on a substrate composed mainly of silicon oxide and having an amorphous surface in the same manner as in Example 1. This amorphous silicon thin film containing crystalline silicon clusters was irradiated with an energy beam from the surface side excluding locally the small region of 0.7  $\mu\text{m}$  diameter to obtain the starting thin film. In this Example, being different from Example 1, in the energy beam-irradiated region, the crystalline silicon clusters contained in the thin film were completely made amorphous.

[0074] This starting thin film was irradiated with a ArF excimer laser beam at an energy density of about 210 mJ/cm<sup>2</sup> for 30 nsec for melting and resolidification to obtain a crystalline thin film.

45 [0075] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 0.7  $\mu\text{m}$  diameter as the center to a size of about 1.5  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 40 nm.

[0076] In the starting thin film of this Example, in the small region of about 0.7  $\mu\text{m}$  diameter which was not irradiated with the energy beam, a crystalline cluster was present, but not present in the other portion. This difference in the size distribution of number concentration of crystalline clusters causes the difference in the state among the continuously placed regions to form "small region 1" of about 0.7  $\mu\text{m}$  diameter as shown in Figs. 1A to 1H. By in-situ observation of the melting-resolidification process of the energy beam-irradiated thin film made completely amorphous and of a non-

irradiated thin film under the aforementioned melting-resolidification conditions, the energy beam-irradiated thin film was confirmed to be melted completely, whereas the non-irradiated thin film was confirmed to be melted incompletely, and the former was melted at a temperature lower than the latter.

[0077] As described above, in this Example, the starting thin film was an amorphous thin film containing crystalline clusters, and the size distribution of number concentration of crystalline clusters was different between the regions in the amorphous base material of the starting thin film. Thereby, in the melting-resolidification of the starting thin film, the size distribution of number concentration of the remaining unmelted clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

10 [Example 3]

[0078] A third crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 3 of the present invention.

[0079] A polycrystalline silicon thin film was formed in a thickness of 80 nm by vapor deposition on a substrate having an amorphous surface composed of an inorganic silicon compound, as the starting thin film. The deposition of the starting film was conducted by a selective deposition means to obtain an average diameter of about 300 nm in a small region of about 2  $\mu\text{m}$  square, and of about 100 nm in other region in the thin film plane.

[0080] This starting thin film was irradiated with an XeCl excimer laser beam at an energy density of about 300 mJ/cm<sup>2</sup> for 40 nsec for melting and resolidification to obtain a crystalline thin film.

[0081] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that single crystal grain had grown around the above small region of about 2  $\mu\text{m}$  square as the center to a grain size of about 3  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 100 nm.

[0082] In the starting thin film of this Example, in the small region of about 2  $\mu\text{m}$  square, the average value of the size distribution of number concentration of crystal grains or crystalline clusters is higher than in the other regions. This difference in the size distribution of number concentration of crystal grains makes difference in the state among the continuously placed regions to form "small region 1" of about 2  $\mu\text{m}$  square as shown in Figs. 1A to 1H. By in-situ observation of the melting-resolidification process of the polycrystalline silicon thin films of an average diameter of about 100 nm and of an average diameter of about 300 nm under the aforementioned melting-resolidification conditions, the former was confirmed to be completely melted, whereas the latter was confirmed to be incompletely melted.

[0083] As described above, in this Example, the starting thin film was a polycrystalline thin film containing crystal grains or crystalline clusters, and the size distribution of number concentration of crystalline clusters was different between the regions. Thereby, in the melting-resolidification of the starting thin film, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 4]

[0084] A fourth crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 4 of the present invention.

[0085] A polycrystalline silicon thin film was formed in a thickness of 80 nm by vapor deposition on a substrate having an amorphous surface composed of an inorganic silicon compound. Then, this polycrystalline silicon thin film was irradiated with an energy beam from the surface side excluding locally a small region of about 0.7  $\mu\text{m}$  diameter to make amorphous in the same manner as in Example 2. This was employed as the starting thin film.

[0086] This starting thin film was irradiated with an XeCl excimer laser beam at an energy density of about 250 mJ/cm<sup>2</sup> for 40 nsec for melting and resolidification to obtain a crystalline thin film.

[0087] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 0.7  $\mu\text{m}$  diameter as the center to a size of about 2  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 60 nm.

[0088] In the starting thin film of this Example, the small region of about 0.7  $\mu\text{m}$  diameter was polycrystalline, and the other region was amorphous. This difference makes the difference in the states among the continuously placed regions to form "small region 1" of about 0.7  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. By in-situ observation of the melting-resolidification process of the amorphous silicon thin film and the polycrystalline silicon thin film, the former was confirmed to be completely melted, whereas the latter was confirmed to be incompletely melted. The former was melted at a temperature lower than the latter.

[0089] As described above, in this Example, the crystalline thin film has an amorphous region and a polycrystalline region, and the crystallinity was different between the regions. Thereby, in the melting-resolidification of the starting

thin film, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 5]

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**[0090]** A fifth crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 5 of the present invention.

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**[0091]** A polycrystalline silicon thin film was formed in a thickness of 80 nm by vapor deposition on a substrate having an amorphous surface composed of an silicon oxide in the same manner as in Example 4. Then this thin film was doped with phosphorus excluding locally a small region of about 1.5  $\mu\text{m}$  diameter. This was employed as the starting thin film.

**[0092]** This starting thin film was irradiated with an XeCl excimer laser beam at an energy density of about 350 mJ/cm<sup>2</sup> for 40 nsec for melting and resolidification to obtain a crystalline thin film.

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**[0093]** The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby, it was found that a single crystal grain had grown around the above small region of about 1.5  $\mu\text{m}$  diameter as the center to a size of about 2.5  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 150 nm.

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**[0094]** In the starting thin film of this Example, the small region of about 1.5  $\mu\text{m}$  diameter contained little phosphorus, whereas the other region contained phosphorus. This difference makes the difference in the state among the continuously placed regions to form "small region 1" of about 1.5  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. By in-situ observation of the melting-resolidification process of the polycrystalline silicon thin film containing phosphorus and the polycrystalline silicon thin film not containing phosphorus, the former was confirmed to be completely melted, whereas the latter was confirmed to be incompletely melted. The former was melted at a temperature lower than the latter.

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**[0095]** Further, in the phosphorus-containing polycrystalline thin film, the impurity phosphorus was confirmed to segregate at the grain boundary.

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**[0096]** As described above, in this Example, the starting thin film was a polycrystalline silicon thin film containing an impurity, having regions of different melting points depending on the impurity concentration. Thereby, in the melting-resolidification of the starting thin film, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters was different between the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 6]

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**[0097]** A sixth crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 6 of the present invention.

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**[0098]** An amorphous thin film containing neither a crystal grain nor a crystalline cluster was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of silicon oxide. Then, this amorphous silicon thin film was irradiated with an energy beam from the surface side excluding locally a small region of about 0.6  $\mu\text{m}$  diameter to change the state of the interface between the amorphous silicon thin film and the underlying substrate in the irradiated region. This was employed as the starting thin film.

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**[0099]** This starting thin film was irradiated with an KrF excimer laser beam at an energy density of about 310 mJ/cm<sup>2</sup> for 30 nsec for melting and resolidification to obtain a crystalline thin film.

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**[0100]** The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 0.6  $\mu\text{m}$  diameter as the center to a size of about 3  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

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**[0101]** In the starting thin film of this Example, the state of the interface between the thin film and the underlying substrate is different at the small region of about 0.6  $\mu\text{m}$  diameter from that in the other region. This difference differentiates the state between the continuously placed regions to form "small region 1" of about 0.6  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. The change of the interface state by energy beam irradiation enhances the free-energy barrier to nucleation of crystallite in the solid-phase crystallization of the amorphous silicon thin film. As the result, in the solid-phase crystallization before the melting of the starting thin film, the crystal nucleus is formed in the solid phase preferentially in the small region of about 0.6  $\mu\text{m}$  diameter, and the crystal grain grows to the outside of the small region. Thereafter, with temperature rise of the thin film, at the region other than the small region, fine crystal grains are formed at a high nucleation rate. In the process of melting, the large crystal grains formed in the solid-phase crystallization in the small region of about 0.6  $\mu\text{m}$  diameter is melted incompletely, whereas the fine crystal grains having formed in a high density outside the small region are completely melted.

**[0102]** As described above, in this Example, the starting thin film is an amorphous thin film, and the state of the

interface between the thin film and the substrate is changed between the regions. Therefore, in melting-resolidification of the starting thin film, the height of free-energy barrier to crystallite nucleation in the solid phase crystallization before the melting varies with the region. Thereby, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 7]

[0103] A seventh crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 7 of the present invention.

[0104] An amorphous silicon thin film was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of silicon oxide. Then this thin film was doped with tin locally in a small region of about 1  $\mu\text{m}$  square. This was employed as the starting thin film.

[0105] This starting thin film was irradiated with an Ar<sup>+</sup> laser beam at an energy density of about 500 mJ/cm<sup>2</sup> for 10 sec for melting and resolidification to obtain a crystalline thin film.

[0106] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 1  $\mu\text{m}$  square as the center to a grain size of about 2.5  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 40 nm.

[0107] In the starting thin film of this Example, the concentration of impurity tin is different at the small region of about 1  $\mu\text{m}$  square from that in the other region. This difference differentiates the states between the continuously placed regions to form "small region 1" of about 1  $\mu\text{m}$  square shown in Figs. 1A to 1H. The impurity like tin lowers the free-energy barrier to nucleation of crystallite in the solid-phase crystallization of the amorphous silicon thin film. As the result, in the solid-phase crystallization before the melting of the starting thin film, the crystal nucleus is formed in the solid phase preferentially in the small region of about 1  $\mu\text{m}$  square, and the crystal grain grows to the outside of the small region. Thereafter, with temperature rise of the thin film, at the region other than the small region, fine crystal grains are formed at a high nucleation rate. In the process of melting, the large crystal grains formed in the solid-phase crystallization in the small region of about 1.2  $\mu\text{m}$  square is melted incompletely, whereas the fine crystal grains having formed in a high density outside the small region are completely melted.

[0108] As described above, in this Example, the starting thin film is an amorphous thin film containing an impurity, and the concentration of the impurity is changed between the regions. Therefore, in the solid-phase crystallization before the melting in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation varies with the region. Thereby, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 8]

[0109] A crystalline silicon-germanium thin film formed by the process shown in Figs. 1A to 1H is described as Example 8 of the present invention.

[0110] An amorphous silicon-germanium thin film of a stoichiometric elemental composition was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of an inorganic silicon compound. To this thin film, germanium was added locally to a small region of about 2  $\mu\text{m}$  diameter on the amorphous silicon-germanium thin film. This was employed as the starting thin film.

[0111] This starting thin film was irradiated with an Ar<sup>+</sup> laser beam at an energy density of about 400 mJ/cm<sup>2</sup> for 5 sec for melting-resolidification. The resulting resolidified thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 2  $\mu\text{m}$  diameter as the center to a size of about 4  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 100 nm.

[0112] In the starting thin film of this Example, the elemental composition ratio in the small region of about 2  $\mu\text{m}$  diameter was different from that in the other portion. This difference causes the difference in the states between the continuously placed regions to form "small region 1" of about 2  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. The free-energy barrier to nucleation of crystallite in the solid-phase crystallization of the amorphous silicon-germanium thin film varies with the elemental composition ratio of silicon to germanium, reaching the maximum near the stoichiometric ratio. As the result, in the solid-phase crystallization before the melting of the starting thin film, the crystal nucleus is formed in the solid phase preferentially in the small region of about 2  $\mu\text{m}$  diameter, and the crystal grain grows to the outside of the small region. Thereafter, with temperature rise of the thin film, at the region outside the small region, fine crystal grains are formed at a high nucleation rate. In the process of melting, the large crystal grains formed in the solid-phase

crystallization in the small region of about 2  $\mu\text{m}$  diameter is melted incompletely, whereas the fine crystal grains having formed at a high density outside the small region are completely melted.

[0113] As described above, in this Example, the starting thin film is an amorphous thin film having the elemental composition ratio changed with the regions. Therefore, in the solid-phase crystallization before the melting in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation varies with the region. Thereby, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 9]

[0114] An eighth crystalline silicon thin film formed by the process shown in Figs. 1A to 1H is described as Example 9 of the present invention.

[0115] An amorphous silicon thin film was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of silicon oxide. Then on the surface thereof, a palladium thin film was formed at a thickness of 5 nm locally onto the surface of a small region of about 1  $\mu\text{m}$  diameter. This was employed as the starting thin film.

[0116] This starting thin film was irradiated with a YAG laser beam at an energy density of about 250 mJ/cm<sup>2</sup> for 10 sec for melting and resolidification to obtain a crystalline thin film.

[0117] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 1  $\mu\text{m}$  diameter as the center to a size of about 5  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 40 nm.

[0118] In the starting thin film of this Example, palladium is adsorbed on the surface in the small region of about 1  $\mu\text{m}$  diameter, and not in the other region. This difference in the surface adsorbate differentiates the states between the continuously placed regions to form "small region 1" of about 1  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. The adsorption of metal atoms like palladium and nickel on the thin film surface lowers the free-energy barrier to nucleation of crystallite in the solid-phase crystallization of the amorphous silicon thin film. As the result, in the solid-phase crystallization before the melting of the starting thin film, the crystal nucleus is formed in the solid phase preferentially in the small region of about 1  $\mu\text{m}$  diameter, and the crystal grain grows to the outside of the small region. Thereafter, with temperature rise of the thin film, in the region outside the small region, fine crystal grains are formed at a high nucleation rate. In the process of melting, the large crystal grains formed in the solid-phase crystallization in the small region of about 1  $\mu\text{m}$  diameter is melted incompletely, whereas the region outside the small region, constituted of high density fine crystal grains, is completely melted.

[0119] As described above, in this Example, the starting thin film was an amorphous thin film having an adsorbate in a limited region. Therefore, in the solid-phase crystallization before the melting in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation varies with the region. Thereby, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 10]

[0120] A crystalline germanium thin film formed by the process shown in Figs. 1A to 1H is described as Example 10 of the present invention.

[0121] An amorphous germanium thin film was formed in a thickness of 200 nm by vapor deposition on a substrate having an amorphous surface composed of an inorganic silicon compound. Then on the surface thereof, an indium thin film was formed in a thickness of 10 nm locally in a small region of about 2  $\mu\text{m}$  diameter. This was employed as the starting thin film. This starting thin film was irradiated with a GGG laser beam at an energy density of about 300 mJ/cm<sup>2</sup> for 1 sec for melting and resolidification to obtain a crystalline thin film.

[0122] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown around the above small region of about 2  $\mu\text{m}$  diameter as the center to a size of about 5  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 80 nm.

[0123] In the starting thin film of this Example, indium was adsorbed on the surface in the limited small region of about 2  $\mu\text{m}$  diameter, and not in the other region. This difference in the surface adsorbate differentiates the state between the continuously placed regions to form "small region 1" of about 2  $\mu\text{m}$  diameter shown in Figs. 1A to 1H. The adsorption of metal atoms like indium on the thin film surface lowers the free-energy barrier to nucleation of crystallite in the solid-phase crystallization of the amorphous germanium thin film. As the result, in the solid-phase crystallization before the melting of the starting thin film, the crystal nucleus is formed in the solid phase preferentially in the small

region of about 2  $\mu\text{m}$  diameter, and the crystal grain grows to the outside of the small region. Thereafter, with temperature rise of the thin film, in the region other than the small region, fine crystal grains are formed at a high nucleation rate. In the process of melting, the large crystal grains formed in the solid-phase crystallization at the small region of about 2  $\mu\text{m}$  diameter is melted incompletely, whereas the region outside the small region, constituted of fine crystal grains in a high density is completely melted.

[0124] As described above, in this Example, the starting thin film was an amorphous thin film having an adsorbate in a limited region. Therefore, in the solid-phase crystallization before the melting in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation varies with the region. Thereby, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 11]

[0125] A crystalline silicon thin film formed by the process shown in Figs. 2A to 2H is described as Example 11 of the present invention.

[0126] An amorphous silicon thin film was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of silicon oxide. Then this thin film was doped with tin locally in small regions of about 2  $\mu\text{m}$  square. This was employed as the starting thin film.

[0127] This starting thin film was irradiated with an KrF excimer laser beam at an energy density of about 400 mJ/cm<sup>2</sup> for 40 nsec for melting and resolidification to obtain a crystalline thin film. Under the above conditions, the entire thin film was completely melted.

[0128] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown respectively around the above small regions of about 2  $\mu\text{m}$  square as the center to a grain size of about 4  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 30 nm.

[0129] In the starting thin film of this Example, the concentration of impurity in the small region of about 2  $\mu\text{m}$  square is different from that in the other region. This difference causes the difference in the states between the continuously placed regions to form "small region 1" of about 2  $\mu\text{m}$  square shown in Fig. 1A. The impurity like tin lowers also the free-energy barrier to nucleation of crystallite in the molten silicon thin film, similarly as the effect in the solid-phase crystallization of the amorphous silicon thin film. As the result; in the resolidification process after complete melting of the starting thin film, the crystal nuclei are formed in the molten silicon preferentially in the small regions of about 2  $\mu\text{m}$  square, and the crystal grains grow to the outside of the small regions.

[0130] As described above, in this Example, the starting thin film is an amorphous thin film containing an impurity, and the concentration of the impurity is changed between the regions. Therefore, in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation from the molten matrix varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location.

[Example 12]

[0131] A crystalline silicon-germanium thin film formed by the process shown in Figs. 2A to 2H is described as Example 12 of the present invention.

[0132] An amorphous silicon-germanium thin film of a stoichiometric elemental composition was formed in a thickness of 100 nm by vapor deposition on a substrate having an amorphous surface composed of an inorganic silicon compound in the same manner as in Example 8. To this thin film, silicon was added locally to small regions of about 2  $\mu\text{m}$  diameter on the amorphous silicon-germanium thin film. This was employed as the starting thin film.

[0133] This starting thin film was irradiated with an XeCl excimer laser beam at an energy density of about 350 mJ/cm<sup>2</sup> for 40 nsec for melting-resolidification to obtain a crystalline thin film. Under the above conditions, the entire thin film was completely melted.

[0134] The resulting crystalline thin film was examined regarding the crystal grain shape. Thereby it was found that a single crystal grain had grown respectively around the above small regions of about 2  $\mu\text{m}$  diameter as the center to a size of about 4  $\mu\text{m}$  diameter. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 60 nm.

[0135] In the starting thin film of this Example, the elemental composition ratio in the small region of about 2  $\mu\text{m}$  diameter was different from that in the other portion. This difference causes the difference in the states of the continuously placed regions to form "small region 1" of about 2  $\mu\text{m}$  diameter shown in Fig. 2A. The free-energy barrier to nucleation of crystallite in the solidification of the molten silicon-germanium thin film varies with the elemental composition ratio of silicon to germanium, reaching the maximum near the stoichiometric ratio, as well as nucleation in the solid-phase crystallization. As the result, the crystal nuclei are formed, in the resolidification process after the complete

melting, preferentially in the small region of about 2  $\mu\text{m}$  diameter, and the crystal grain grows larger to the outside of the small region.

[0136] As described above, in this Example, the starting thin film was an amorphous thin film having elemental composition ratio changed with the regions. Therefore, in the melting-resolidification process of the starting thin film, the height of free-energy barrier to crystallite nucleation from the molten matrix varies with the region. Thereby, the crystalline thin film was formed which is constituted of crystal grains controlled in spatial location.

[Example 13]

10 [0137] A crystalline silicon thin film formed by the process shown in Figs. 3A to 3H is described as Example 13 of the present invention.

[0138] A starting thin film was prepared, and melted and resolidified in the same manner as in Example 1 to obtain a crystalline silicon thin film except that the "small region 1" was made larger in size to about 1.5  $\mu\text{m}$  diameter.

15 [0139] The obtained crystalline thin film was observed regarding the shape of the crystal grain constituting the thin film. It was found that two single crystal grains of about 1.5  $\mu\text{m}$  diameter had grown respectively around the small region of about 1.5  $\mu\text{m}$  diameter, and the surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

20 [0140] In this Example, the "small region 1" was enlarged to about 1.5  $\mu\text{m}$  in diameter to increase the possibility of remaining of unmelted crystal grains or crystalline clusters therein in the melting process of the thin film in proportion to the volume increase of "small region 1". Thereby plural crystal grains grew in the small region of about 1.5  $\mu\text{m}$  diameter.

25 [0141] The spatial location of the crystal grains constituting the crystalline thin film is controlled in this Example in the same manner as in Example 1. Incidentally, the increase and control of the number of the crystal grains growing at "small region 1" can be conducted similarly also in Examples 2 to 12. The control can also be conducted not only by the volume of "small region 1" but also by the concentration of the crystal grains or crystalline clusters, and free-energy barrier to crystallite nucleation in the "small region 1" or the melting point, and the heating conditions.

[Example 14]

30 [0142] A crystalline silicon thin film formed by the process shown in Figs. 4A to 4H is described as Example 14 of the present invention.

[0143] A starting thin film was prepared, and was melted and resolidified in the same manner as in Example 1 to obtain a crystalline thin film except that two "small regions 1" of about 1  $\mu\text{m}$  of Fig. 1A were provided at a distance of 10  $\mu\text{m}$ .

35 [0144] The obtained crystalline thin film was observed regarding the shape of the crystal grains constituting the crystalline thin film. It was found that a single crystal grain of about 2  $\mu\text{m}$  diameter grew respectively at the two small regions of about 1  $\mu\text{m}$  diameter, and the surrounding portion including the region of about 8  $\mu\text{m}$  in length between the crystal grains was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

40 [0145] The spatial location of the crystal grains constituting the crystalline thin film in this Example is controlled in the same manner as in Example 1. Incidentally, the melting-resolidification process employing plural "small region 1" can be conducted similarly also in Examples 2 to 12.

[Example 15]

45 [0146] A crystalline silicon thin film formed by the process shown in Figs. 5A to 5H is described as Example 15 of the present invention.

[0147] A starting thin film was prepared, and was melted and resolidified in the same manner as in Example 14 to obtain a crystalline silicon thin film except that the distance between the two "small regions 1" was reduced to about 2  $\mu\text{m}$ . The obtained crystalline thin film was observed regarding the shape of the crystal grains constituting the crystalline thin film produced by melting-resolidification. It was found that a single crystal grain of about 2  $\mu\text{m}$  diameter grew respectively in the two small regions of about 1  $\mu\text{m}$  diameter, and the two crystal grains came to be in contact with each other at the growth fronts to form a grain boundary. The surrounding portion was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

55 [0148] The spatial location of the crystal grains constituting the crystalline thin film in this Example is controlled in the same manner as in Example 1. Incidentally, the increase and control of the number of the crystal grain growing in "small region 1" can be conducted similarly also in Examples 2 to 12. The melting-resolidification with plural "small regions 1" provided at a short distance as in this Example can be conducted similarly also in Examples 2 to 12.

## [Example 16]

[0149] A crystalline silicon thin film formed by the process shown in Figs. 6A to 6H is described as Example 16 of the present invention.

5 [0150] A starting thin film was prepared, and was melted and resolidified in the same manner as in Example 14 to obtain a crystalline silicon thin film except that the same "small regions 1" of about 1  $\mu\text{m}$  diameter as in Fig. 1A were formed in plurality periodically at intervals of 2  $\mu\text{m}$  in a square lattice pattern.

10 [0151] The obtained crystalline thin film was observed regarding the shape of the crystal grains constituting the crystalline thin film. It was found that a single crystal grain had grown respectively in an average diameter of about 2  $\mu\text{m}$  at the lattice points of the square lattice of 2  $\mu\text{m}$  intervals, and the crystal grains came to be in contact with each other at the growth fronts to form grain boundaries. The thin film were filled with the locationally controlled crystal grains without formation of a random fine crystal grain region.

15 [0152] The spatial location of the crystal grains constituting the crystalline thin film in this Example is controlled in the same manner as in Example 1. The melting-resolidification by employing plural "small regions 1" can be conducted similarly also in Examples 2 to 12.

## [Example 17]

20 [0153] A crystalline silicon thin film formed by the process shown in Figs. 7A to 7H is described as Example 17 of the present invention.

[0154] A polycrystalline silicon thin film was formed in a thickness of 10 nm by vapor deposition on a substrate having an amorphous surface composed of silicon oxide. Then this polycrystalline thin film was removed by leaving small regions of about 1  $\mu\text{m}$  diameter at lattice points of a square lattice of 2  $\mu\text{m}$  spacings. Further thereon, an amorphous silicon thin film was formed in a thickness of 100 nm by vapor deposition. This was employed as the starting thin film.

25 [0155] This starting thin film was irradiated with an KrF excimer laser beam at an energy density of about 310 mJ/  $\text{cm}^2$  for 30 nsec for melting and resolidification to obtain a crystalline thin film.

30 [0156] The resulting crystalline thin film was examined regarding the shape of the constituting crystal grains. Thereby it was found that a single crystal grain of about 2  $\mu\text{m}$  in average diameter had grown at the respective lattice points of the square lattice of 2  $\mu\text{m}$  spacings, and the crystal grains came to be in contact with each other at the growth fronts to form a grain boundary. The thin film were filled with the locationally controlled crystal grains without formation of a random fine crystal grain region.

35 [0157] As described above, in this Example, the starting thin film has polycrystalline regions and amorphous region. Therefore, in the melting-resolidification process of the starting thin film, the size distribution of number concentration of the remaining unmelted crystal grains or crystalline clusters varies with the regions to form a crystalline thin film constituted of crystal grains controlled in spatial location.

40 [0158] The melting-resolidification by employing three-dimensional "small regions 1" can be applied similarly to the crystalline thin films of Examples 2 to 12.

## [Example 18]

[0159] A crystalline silicon thin film formed by the process shown in Figs. 8A to 8H is described as Example 18 of the present invention.

45 [0160] A starting thin film which was the same as the one in Example 1 was formed on a substrate having an amorphous surface composed of silicon oxide. Then a most part of this polycrystalline thin film was removed by retaining a small region of about 10  $\mu\text{m}$  diameter including a small region of about 1  $\mu\text{m}$  diameter corresponding to "small region 1" in Fig. 1. This was employed as the starting thin film in this Example.

50 [0161] This starting thin film was irradiated with an KrF excimer laser beam at an energy density of about 240 mJ/  $\text{cm}^2$  for 30 nsec for melting and resolidification to obtain a crystalline thin film.

[0162] The resulting crystalline thin film was examined regarding the shape of the constituting crystal grains. Thereby it was found that a single crystal grain of about 3  $\mu\text{m}$  diameter had grown around the small region of about 1  $\mu\text{m}$  diameter as the center. Except the portion of the single crystal grain having grown from the small region of about 1  $\mu\text{m}$  diameter, the thin film of 10  $\mu\text{m}$  diameter remaining on the substrate was filled randomly with various sizes of fine crystal grains of an average diameter of about 50 nm.

55 [0163] The spatial location of the crystal grain constituting the crystalline thin film in this Example is controlled in the same manner as in Example 1. The melting-resolidification process by utilizing a starting thin film formed on a limited area of the substrate as in this Example can be applied similarly to the crystalline thin films of Examples 2 to 17.

## [Example 19]

[0164] A crystalline silicon thin film formed by the process shown in Figs. 9A to 9H is described as Example 19 of the present invention.

5 [0165] The crystalline thin film was obtained by melting-resolidification in the same manner as in Example 18 except that the thin film was removed by retaining a region of about 4  $\mu\text{m}$  square including a small region of about 1  $\mu\text{m}$  diameter as "small region 1".

10 [0166] The resulting crystalline thin film was examined regarding the shape of the constituting crystal grains. Thereby it was found that a single crystal grain constituted the entire area of the thin film of 4  $\mu\text{m}$  square, and there was no crystal boundary.

[0167] The spatial location of the crystal grain constituting the crystalline thin film was controlled in the same manner as in Example 1. The melting-resolidification for forming a single crystal grain as in this example can be applied also to the crystalline thin films of Examples 2 to 17.

## 15 [Example 20]

[0168] A magnetic recording element having a constitution shown in Fig. 10, and a device employing the element are described as Example 19 of the present invention.

20 [0169] A cobalt-chromium thin film of 50 nm thick was deposited by sputtering on a glass disk substrate having been surface-treated. Chromium was applied as a dopant to regions of about 0.1 mm square at region intervals of 2 mm on concentric circles of circle intervals of 2 mm having a center common to the disk. This was employed as the starting thin film. The small regions doped with chromium corresponded to "small region 1" in Fig. 10.

25 [0170] This starting thin film was irradiated with a pulse light beam having light emission wavelength maximum in an infrared region to form crystalline thin films by melting-resolidification. Thereby, crystal grains grew in a partial arc shape around the regions of about 0.1 mm square doped with chromium.

[0171] A hard disk device was built with this disk as the recording medium in which the respective crystal grains in the partial arc shape constitute a physical sector. With this hard disk device, the error rate was decreased by half in comparison with the hard disk device of the same configuration employing a disk not doped locally with chromium.

30 [0172] In this Example, the regions doped with chromium corresponds to the small regions 1, the partial arc-shaped crystal grain corresponds to crystal grain 6, and the magnetic pick-up corresponds to active small region 10 in Fig. 10.

35 [0173] The starting thin film in this Example is an amorphous thin film in which the elemental composition ratio varies with the regions. Therefore, in melting-resolidification of the starting thin film, the free-energy barrier to crystallite nucleation during solidification from the molten matrix is varied with the region to give a crystalline thin film constituted of crystal grains controlled in spatial location. The device of this Example was built by employing a crystalline thin films constituted of elements having a special region formed by a single crystal grain.

## [Example 21]

40 [0174] A crystalline superconductive thin film having a constitution shown in Fig. 11, and a grain-boundary Josephson junction type of superconductive logic element are described as Example 21 of the present invention.

[0175] A polycrystalline thin film of a superconductive oxide material  $\text{BaPb}_{0.7}\text{Bi}_{0.3}\text{O}_2$  was deposited in a thickness of 100 nm by sputtering on a sapphire substrate kept at 260°C. This thin film was irradiated with an energy beam from the surface except the regions of 0.25  $\mu\text{m}$  square arranged at lattice points of a square lattice of 1  $\mu\text{m}$  spacings in the thin film to amorphize the irradiated regions. This is employed as the starting thin film of this Example. The small regions which had not been irradiated with the energy beam constitute "small regions 1" in Fig. 11.

45 [0176] The starting thin film was irradiated with pulse-modified second harmonic of an YAG laser beam for melting-resolidification to form a crystalline thin film. Thereby crystal grains grew to form grain boundaries in a lattice with spacings of 2  $\mu\text{m}$ . This thin film was confirmed to have superconducting transition at 9K from measurement of temperature dependency of volume resistivity of the thin film. Further, formation of a tunnel type of Josephson junction was confirmed at the grain boundaries from the current-voltage characteristics.

50 [0177] This thin film was patterned to leave the 10  $\mu\text{m}$  square island regions by conventional photolithography to connect electrode terminals composed of polycrystalline  $\text{BaPb}_{0.75}\text{Bi}_{0.25}\text{O}_2$  with the square island region. Thereby a superconductive logic element was fabricated in which a pair of electrode was connected to the two counterposed sides of the square island region as the control terminal, and another pair of electrodes was connected to the remaining sides of the square island as the gate terminal.

55 [0178] From the measurement of fundamental properties of this element, the maximum Josephson current was 200  $\mu\text{A}$ , the voltage allowing the gate current to exceed the above current was 1.8 mV, which were preferred to those employing a simple polycrystalline  $\text{BaPb}_{0.7}\text{Bi}_{0.3}\text{O}_2$  thin film as deposited. The variation of the properties of the plural

elements of the same shape and size were decreased by half in comparison with those employing a simple polycrystalline  $\text{BaPb}_{0.7}\text{Bi}_{0.3}\text{O}_2$  thin film as deposited. These advantages result from the facts that the grain boundary in the 10  $\mu\text{m}$  square active region, namely the total length of the Josephson junction, is constant in any of the elements, and the serial-parallel connection of the crystal grains is constant owing to the junction positions arranged at the square lattice.

5 Thus the effects of the crystalline thin film and the elements of the present invention are obvious.

[0179] In this Example, the regions not irradiated with the energy beam forms small regions 1 in Fig. 11, and the 10  $\mu\text{m}$  square island region corresponds to active small region 10.

[0180] As described above, in this Example, the starting crystalline thin film has amorphous regions and polycrystalline regions. Therefore, in the melting-resolidification process of the starting thin film, the size distribution of number 10 concentration of the remaining unmelted crystal grains varies with the region to form a crystalline thin film constituted of crystal grains controlled in spatial location. The present Example also shows formation of an element employing the crystal grains having controlled spatial location.

[Example 22]

[0181] A MOS type TFT having a construction shown in Fig. 12 is described as Example 22 of the present invention.

[0182] A single silicon crystal grain of 4  $\mu\text{m}$  square was formed on a glass substrate having on the surface a silicon nitride film made of silicon oxide and oxide film thereon using the process described in Example 19. Thereon a gate insulation film made of silicon oxide and a gate electrode film were deposited using a conventional low-temperature process for silicon thin film transistors fabrication. The gate electrode film layer was removed except the region of 1  $\mu\text{m}$  width at the central portion of the single crystal grain. The other portion was doped with boron by a self-alignment method employing the remaining gate electrode film as the mask to form a gate region, a source region, and a drain region. Thereby, the entire area of the gate region was included in the single crystal grain. Thereon, a passivation layer formed by the insulation film was deposited, and an openings were provided through the passivation layer on the respective regions. Finally, aluminum wiring layer was deposited and was patterned to form a gate electrode, source electrode and a drain electrode to obtain a MOS type TFT.

[0183] From measurement of the electrical characteristics, the resulting MOS type TFT of the present invention showed more than twice higher mobility than the conventional elements formed by the same process in the same element shape on a random polycrystalline thin film without "small range 1". As to the variation of the characteristics, the variation of the mobility was reduced by half, and that of the threshold voltage was lowered by a factor of about 1/4.

[0184] In this Example, the spatial location of the crystal grain constituting the crystalline thin film was controlled in the same manner as in Example 19. This Example shows a constitution of the element having an active region in a single-crystal grain in a crystalline thin film.

[Example 23]

[0185] A MOS type TFT having a construction shown in Fig. 13 is described as Example 23 of the present invention.

[0186] A crystalline thin film was obtained by melting-resolidification according to the process described in Example 18 employing a starting rectangle thin film of 3  $\mu\text{m} \times 10 \mu\text{m}$  having a "small region 1" of about 1  $\mu\text{m}$  diameter. The resulting crystalline thin film had a single crystal grain of about 3  $\mu\text{m}$  long in the length direction of the rectangle at the center of the rectangle of 3  $\mu\text{m} \times 10 \mu\text{m}$ , and the other region was filled with fine crystal grains of various sizes of an average diameter of about 50 nm.

[0187] On the above crystalline thin film, a gate insulation film composed of silicon oxide and a gate electrode film were deposited using a conventional silicon TFT low-temperature formation process, and the gate electrode film layer was partly removed except the portion of 1  $\mu\text{m}$  wide including "small region 1" in the center of the single crystal grain of about 3  $\mu\text{m}$  long in the breadth direction kept unremoved. The other portion was doped with boron by a self-alignment method employing the remaining gate electrode film as the mask to form a gate region, a source region, and a drain region. Thereby, the entire area of the gate region was included in the single crystal grain of about 3  $\mu\text{m}$  long in the length direction of the entire area, whereas the source region and the drain region spread over a part of the single crystal grain of about 3  $\mu\text{m}$  long in the length direction and the fine crystalline region of the average diameter of about 50 nm. Thereon, a passivation layer of the insulation film was deposited. An opening was provided on each of the regions through the passivation layer. The openings on the source region and the drain region were limited to extend about 3  $\mu\text{m}$  from the end of the length direction of the rectangular crystalline thin film. Finally, aluminum wiring layer was deposited and was patterned to form a gate electrode, a source electrode and a drain electrode to obtain a MOS type TFT. thereby the source electrode and the drain electrode were connected only to the fine crystal grain region of average diameter of about 50 nm.

[0188] From measurement of the electrical characteristics, the resulting MOS type TFT of the present invention showed more than twice higher mobility than the conventional elements formed by the same process and the same

element shape on a random polycrystalline thin film without "small rage 1". As to the variation of the characteristics, the variation of the mobility was reduced by half, and that of the threshold voltage was lowered remarkably by a factor of about 1/4. The MOS type TFT of this Example had the performance characteristics comparable with that of the element of Example 22, even though the element is not entirely included in the single crystal grain. This means that the channel region as the active region which controls the performance is formed inside the single crystal grain.

[0189] In this Example, the spatial location of the crystal grain constituting the crystalline thin film was controlled in the same manner as in Example 19.

[Example 24].

[0190] An TFT integrated circuit having a construction shown in Fig. 14 is described as Example 24 of the present invention.

[0191] Two elements of the MOS type TFTs shown in Example 22 were formed on one and the same substrate, and the electrodes were connected as follows. The two elements were placed adjacently at a center-to-center distance of 6  $\mu$ m. The drain electrode of the first TFT was connected to the gate electrode of the second TFT. This gate electrode of the second TFT was connected through a condenser element to the source electrode of the same TFT. Thereby an integrated circuit was constructed which comprises the two TFT elements and the condenser element. With this circuit, the output of the source current supplied to the second TFT source through the drain is controlled by the condenser capacity of the condenser element, whereas the storage capacity of the condenser element and the switching of the storage are controlled by the gate voltage of the first TFT. This circuit is applicable, for instance, to a circuit for pixel switching and current control of active matrix type display device.

[0192] By measurement of the basic characteristics, the circuit formed in this Example was compared with a circuit formed on a random polycrystalline thin film formed by the same process in the same circuit shape but not having the "small region 1" of the present invention. As the results, it was confirmed that the circuit can be operated faster more than three times faster in the operable switching frequency range, and that the controllable range of the current outputted from the drain electrode of the second TFT is enlarged by a factor of about 2. The variation of the performances of the same circuits formed in plurality decreased by about half. This means that the variation among the first TFTs and among the second TFTs and the relative property between the first TFT and second TFT in one circuit are small in comparison with the compared circuit.

[0193] In this Example, the spatial location of the crystal grain constituting the crystalline thin film was controlled in the same manner as in Example 19.

[Example 25]

[0194] An EL image display device having a construction shown in Fig. 15 is described as Example 2 of the present invention.

[0195] TFT integrated circuits described in Example 24 were formed as the elementary circuits at points of square lattice of spacings of 100  $\mu$ m on a glass substrate. Wiring was made as below to use the unit cells in the square lattice as pixels of an image display device. Firstly, a scanning line was provided for each one lattice in one axis direction in the square lattice, and the gate electrodes were connected thereto. On the other hand, in the direction perpendicular to the scanning lines, a signal line and a source line were made for each lattice, and the first source electrode and the second source electrode of each of the elements were connected thereto. On the integrated circuits of these elementary circuits, an insulation layer was laminated. Openings were provided to bare the drain electrodes of the second TFTs of the respective elementary circuit. Next, an metal electrode was laminated, and the laminated metal electrode was separated and insulated for each of the pixels.

Finally an electroluminescence (EL) layer and an upper transparent electrode layer were laminated.

Thus, an active matrix type multi-gradation EL image display device was constructed which conducts switching of the pixels and control of input current with the TFT integrated circuit described in Example 24.

[0196] In this image display device, by actuating the first TFT in correspondence with the voltage of the scanning line, an electric charge is stored in the condenser element from the source line corresponding to the current to be input to the signal line, and the current controlled by the second TFT gate voltage corresponding to the stored charge is injected through the source line to the EL-emitting layer.

[0197] The basic characteristics of the image display device of this Example were measured. The image display device of this Example was compared with the one produced with a random polycrystalline thin film in the same process in the same shape without providing the "small region 1" of the present invention. As the results, regarding the static characteristics, it was confirmed that the maximum brightness and the maximum contrast were improved by a factor of about 2, the gradation reproducible region was enlarged by a factor of about 1.5, and the pixel deficiency ratio and luminosity variation was reduced respectively by a factor of 1/3 and 1/2. Regarding the dynamic characteristics, it was

confirmed that the maximum frame rate was improved by a factor of about 2. All of the above improvements come from the improvements of the elementary circuit characteristics and decrease of the variations as mentioned in Example 24, and from improvements in the characteristics of the thin film transistors constituting the element circuit and decrease of variation thereof. Therefore the effects are based on the formation of the active region of the thin transistor in single crystal grain.

**[0198]** As described above in detail, in production of a crystalline thin film by melting-resolidification of a starting thin film, the present invention provides a method for readily controlling spatial location of crystal grains constituting the crystalline thin film by providing regions of different states coexisting continuously in the starting thin film.

**[0199]** The present invention provides also, in production of a crystalline thin film by melting-resolidification of a starting thin film, a method for readily controlling spatial location of crystal grains constituting the crystalline thin film by providing a small region different in the state from the surrounding region in the starting thin film, and allowing growth of a prescribed number of crystal grains or crystalline clusters at the small region.

**[0200]** In the present invention, the regions different in the state from each other can be formed in the starting thin film to control the location of the crystal grain in the crystalline thin film in the melting-resolidification process by any of the methods below: formation of regions different in the size distribution of number concentration of crystal grains or crystalline clusters in the starting thin film; formation of regions in which the melting point is different in a bulk portion or surface of the crystal grains or crystalline clusters, grain boundary between adjacent grains or crystalline clusters, or the amorphous regions; formation of regions different in the size distribution of number concentration of crystal grains or crystalline clusters in the base material of the starting thin film; formation of regions different in the size distribution of number concentration of crystal grains constituting the polycrystalline thin film as the starting thin film; formation of an amorphous region and a polycrystalline region in the starting thin film; formation of regions different in the free-energy barrier to nucleation of crystallite in solidification from the molten phase in the melting-resolidification process; and formation of regions different in the free-energy barrier to nucleation of crystallite in solid-phase crystallization before melting of the amorphous starting thin film. Further in the present invention, the regions can be provided in the starting thin film by forming regions different in the element composition ratio, the impurity concentration contained, or surface adsorbate of the thin film, or different in the interfacial state between the thin film and the substrate.

**[0201]** The present invention provides further a crystalline thin film produced by any of the above-mentioned processes. The crystalline thin film of the present invention has improved performance characteristics with reduced variation thereof by relating spatially the controlled position of constituting crystal grain with a specified region of the element or by forming a special region within a single crystal grain controlled locationally in comparison with conventional crystalline thin film constituted of random crystal grains.

**[0202]** The circuit employing the aforementioned element of the present invention has improved static and dynamic characteristics with reduced variation thereof in comparison with conventional circuits employing a crystalline thin film constituted only of random crystal grains not locationally controlled.

**[0203]** The device of the present invention employing the element or circuit of the present invention is improved in the operational characteristics by improved operational characteristics and reduced variation of the element or circuit. Thus the present invention provides a device of high performance which cannot be realized by conventional device employing a crystalline film constituted only of random crystal grains not controlled locationally.

**[0204]** A process for producing a crystalline thin film is provided which comprises melting and resolidifying a starting thin film having regions different in the state coexisting continuously. A small region of the starting thin film has a size distribution of number concentration of crystal grains or crystalline clusters different from that of the surrounding region. In the process of melting and resolidification, the crystal grain grows preferentially in the one region to control the location of the crystal grain in the crystalline thin film.

#### Claims

1. A process for producing a crystalline thin film, comprising melting and resolidifying a starting thin film having regions different in a state thereof from each other coexisting continuously.
2. A process for producing a crystalline thin film, comprising a step of melting and resolidifying a starting thin film, wherein the starting thin film has a small region different in a state from a surrounding region, and a prescribed number of crystal grains or crystalline clusters grow in the small region.
3. The process for producing a crystalline thin film according to claim 2, wherein the prescribed number of crystal grains or crystalline clusters are nucleated in the small region.
4. The process for producing a crystalline thin film according to claim 2, wherein the prescribed number of crystal

grains or crystalline clusters are crystal grains or crystalline clusters remaining unmelted in the small region at the maximal melting of the starting thin film.

5. The process for producing a crystalline thin film according to claim 1 or 2, wherein the starting thin film contains crystal grain or crystalline cluster, and the state different between the regions is **characterized by** size distribution of number concentration of crystal grains or crystalline clusters remaining unmelted in the step of melting and resolidifying the starting thin film.
10. The process for producing a crystalline thin film according to claim 5, wherein the state different between the regions is **characterized by** a melting point of the bulk portions or surfaces of the crystal grains or crystalline clusters, or boundary between adjacent crystal grains or crystalline clusters.
15. The process for producing a crystalline thin film according to claim 5, wherein the starting thin film contains an amorphous portion, and the state different between the regions is **characterized by** melting point of the bulk portions or surfaces of the crystal grains or crystalline clusters, boundary between adjacent crystal grains or crystalline clusters, or the amorphous portion.
20. The process for producing a crystalline thin film according to claim 5, wherein the starting thin film is an amorphous thin film containing a crystal grain or a crystalline cluster, and the state different between the regions is **characterized by** size distribution of number concentration of the crystal grains or crystalline clusters contained in the amorphous base material of the starting thin film.
25. The process for producing a crystalline thin film according to claim 5, wherein the starting thin film is a polycrystalline thin film, and the state different between the regions is **characterized by** size distribution of number concentration of the crystal grains or crystalline clusters constituting the starting thin film.
30. The process for producing a crystalline thin film according to claim 5, wherein the starting thin film contains an amorphous region and a polycrystalline region in coexistence, and the state different between the amorphous region and the polycrystalline region is **characterized by** crystallinity.
35. The process for producing a crystalline thin film according to claim 5, wherein the starting thin film is an amorphous thin film, and the states different between the regions are **characterized by** the height of free-energy barriers to nucleation of crystallite in solid-phase crystallization before the melting in the melting and resolidification of the starting thin film.
40. The process for producing a crystalline thin film according to claim 11, wherein the height of free-energy barriers to nucleation of crystallite is controlled by any of an element composition ratio, a contained impurity concentration, a surface adsorbate, and a state of the interface between the starting thin film and the substrate.
45. The process for producing a crystalline thin film according to claim 13, wherein the height of free-energy barrier to nucleation of crystallite is controlled by any of an element composition ratio, a contained impurity concentration, a surface adsorbate, and a state of the interface between the starting thin film and the substrate.
50. The process for producing a crystalline thin film according to claim 1 or 2, wherein at least a part of crystal grains having a continuous crystal structure having the crystalline thin film is controlled by controlling the spatial location of the regions different in the state in the starting thin film.
55. 16. A crystalline thin film, produced by the process for producing a crystalline thin film set forth in claim 1 or 2.
17. An element, employing the crystalline thin film set forth in claim 16.
18. The element according to claim 17, wherein the spatial location of at least a part of the crystal grains having a continuous crystal structure in the crystalline thin film is controlled by controlling the spatial location of the regions different in the state in the starting thin film, and the crystal grains having the controlled location are employed as

active regions.

19. The element according to claim 18, wherein the active region is formed inside the single crystal grain in the crystalline thin film.  
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20. A circuit, employing the element set forth in claim 17.
21. A device employing the element set forth in claim 17.
- 10 22. A device employing the circuit set forth in claim 20.

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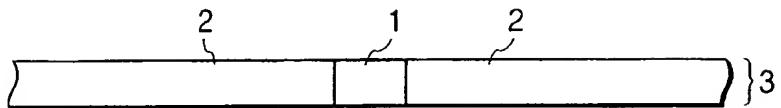
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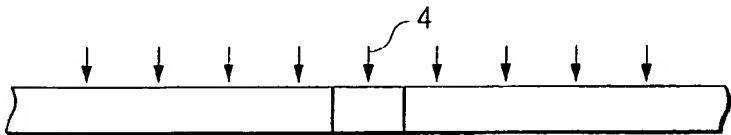
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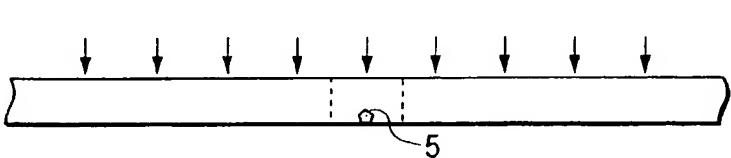
*FIG. 1A*



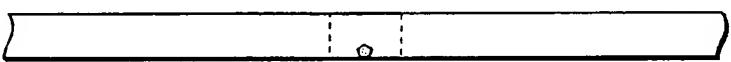
*FIG. 1B*



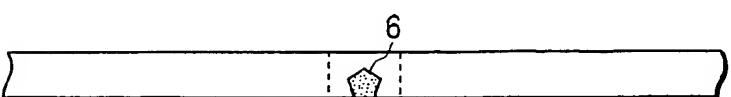
*FIG. 1C*



*FIG. 1D*



*FIG. 1E*



*FIG. 1F*



*FIG. 1G*



*FIG. 1H*



FIG. 2A

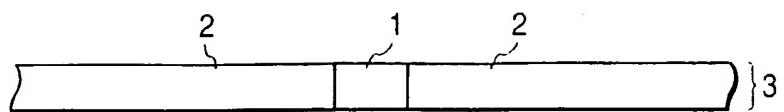


FIG. 2B

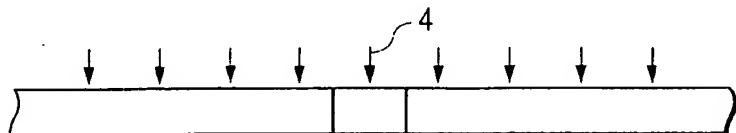


FIG. 2C

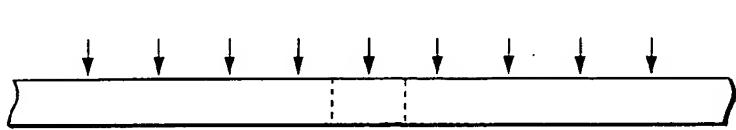


FIG. 2D

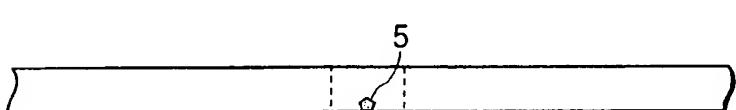


FIG. 2E

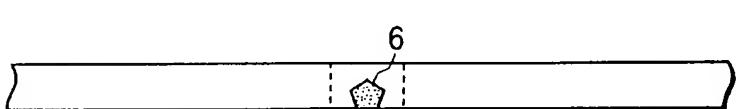


FIG. 2F

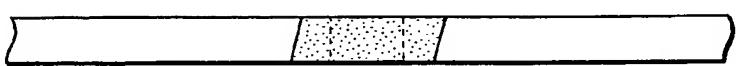


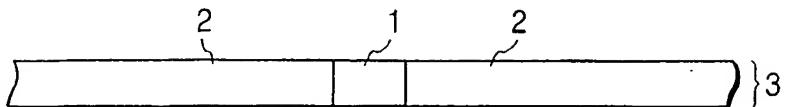
FIG. 2G



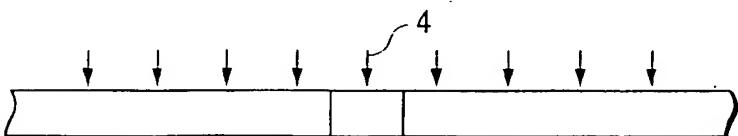
FIG. 2H



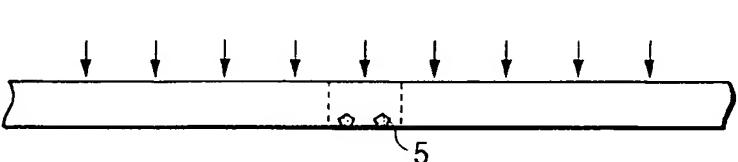
*FIG. 3A*



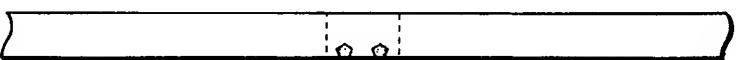
*FIG. 3B*



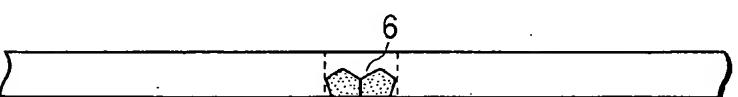
*FIG. 3C*



*FIG. 3D*



*FIG. 3E*



*FIG. 3F*



*FIG. 3G*



*FIG. 3H*

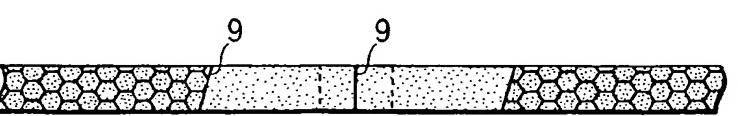


FIG. 4A

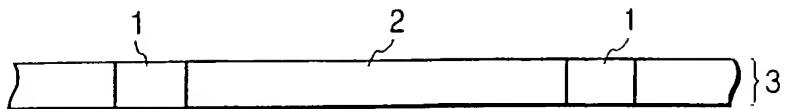


FIG. 4B

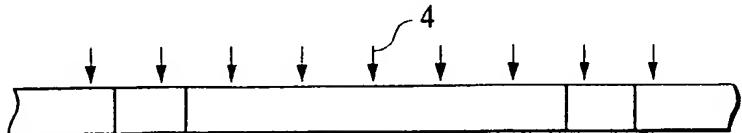


FIG. 4C

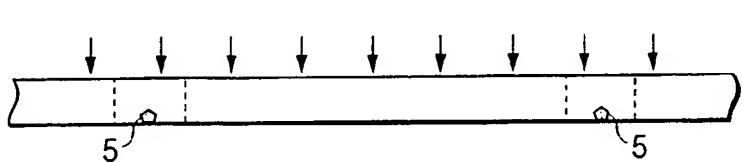


FIG. 4D

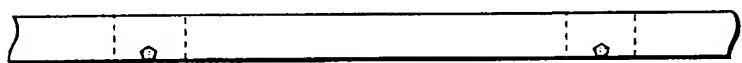


FIG. 4E

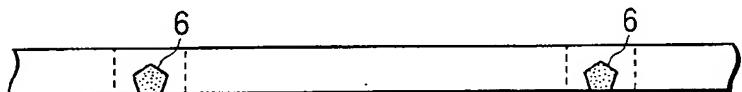


FIG. 4F

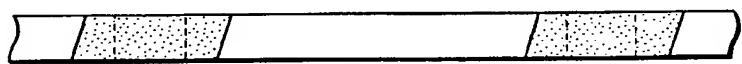


FIG. 4G

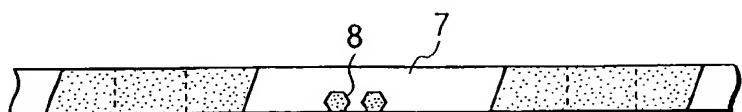


FIG. 4H



FIG. 5A

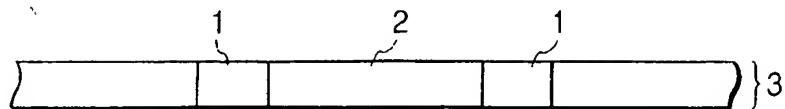


FIG. 5B

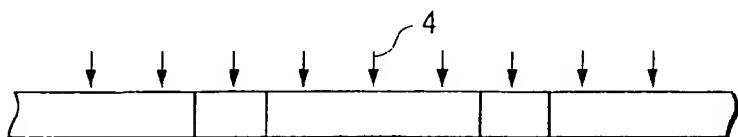


FIG. 5C

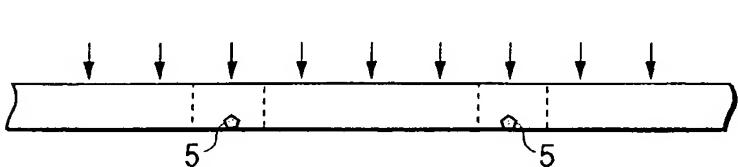


FIG. 5D

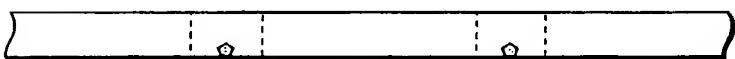


FIG. 5E



FIG. 5F



FIG. 5G

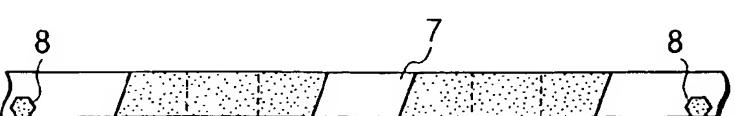
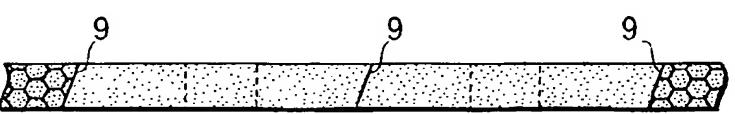
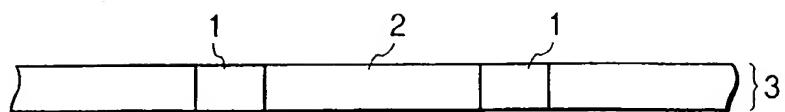


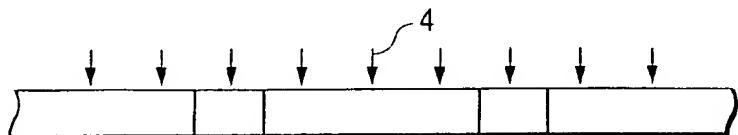
FIG. 5H



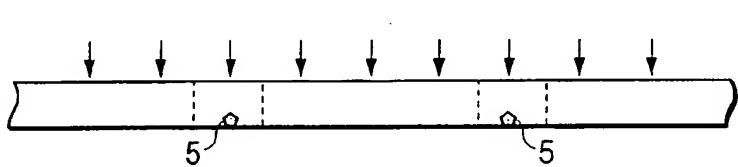
*FIG. 6A*



*FIG. 6B*



*FIG. 6C*



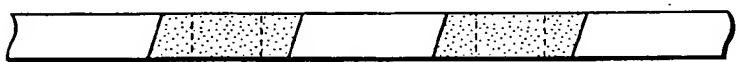
*FIG. 6D*



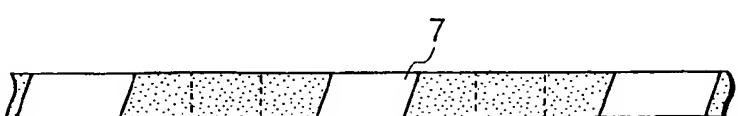
*FIG. 6E*



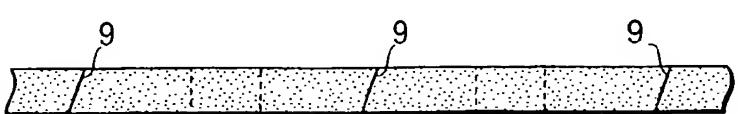
*FIG. 6F*



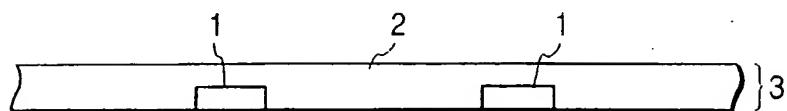
*FIG. 6G*



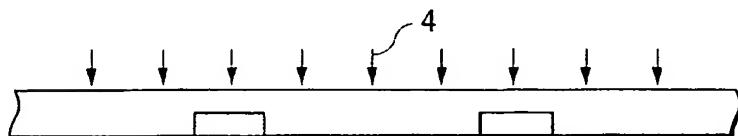
*FIG. 6H*



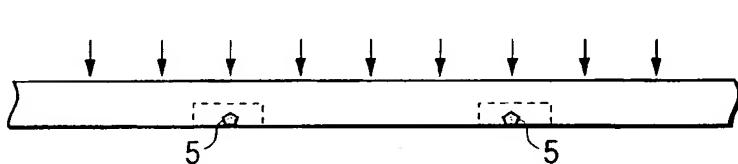
*FIG. 7A*



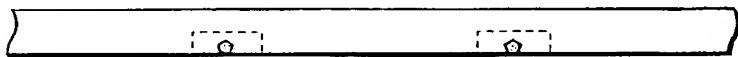
*FIG. 7B*



*FIG. 7C*



*FIG. 7D*



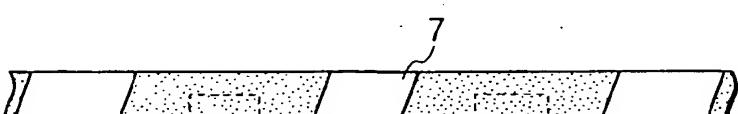
*FIG. 7E*



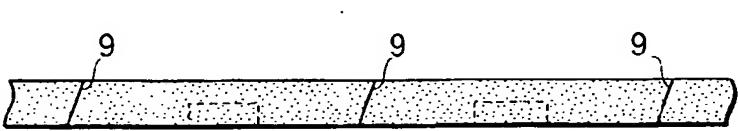
*FIG. 7F*



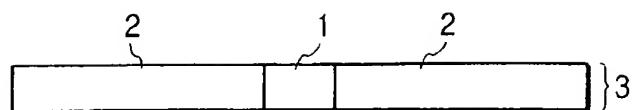
*FIG. 7G*



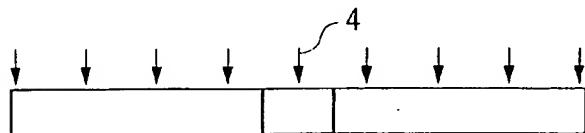
*FIG. 7H*



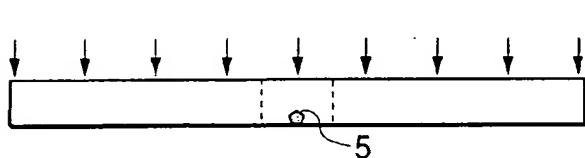
*FIG. 8A*



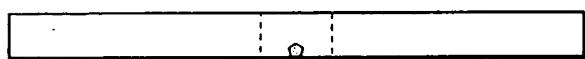
*FIG. 8B*



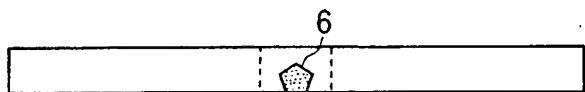
*FIG. 8C*



*FIG. 8D*



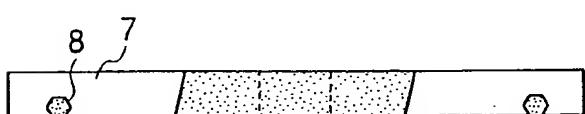
*FIG. 8E*



*FIG. 8F*



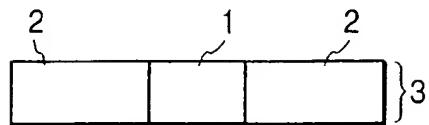
*FIG. 8G*



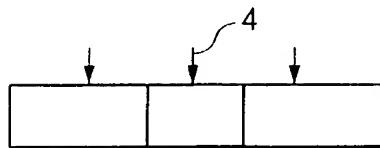
*FIG. 8H*



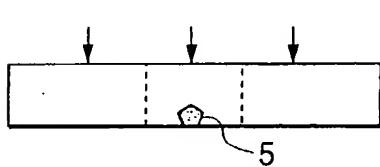
*FIG. 9A*



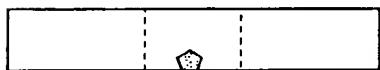
*FIG. 9B*



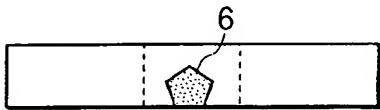
*FIG. 9C*



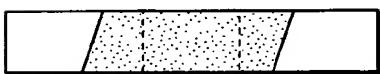
*FIG. 9D*



*FIG. 9E*



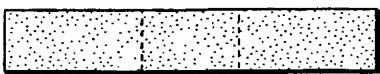
*FIG. 9F*



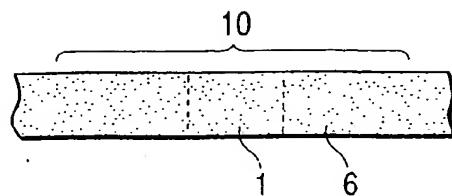
*FIG. 9G*



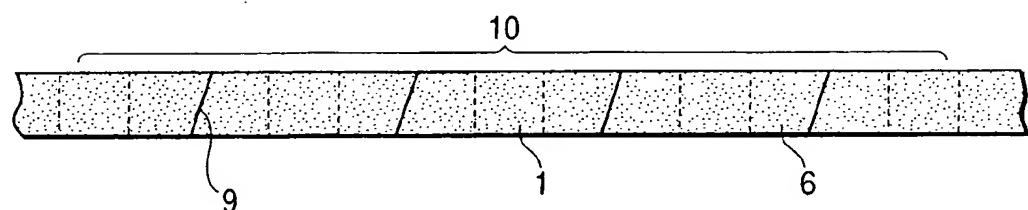
*FIG. 9H*



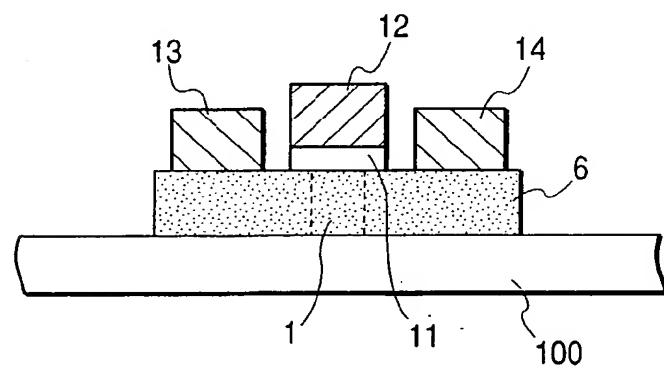
*FIG. 10*



*FIG. 11*



*FIG. 12*



*FIG. 13*

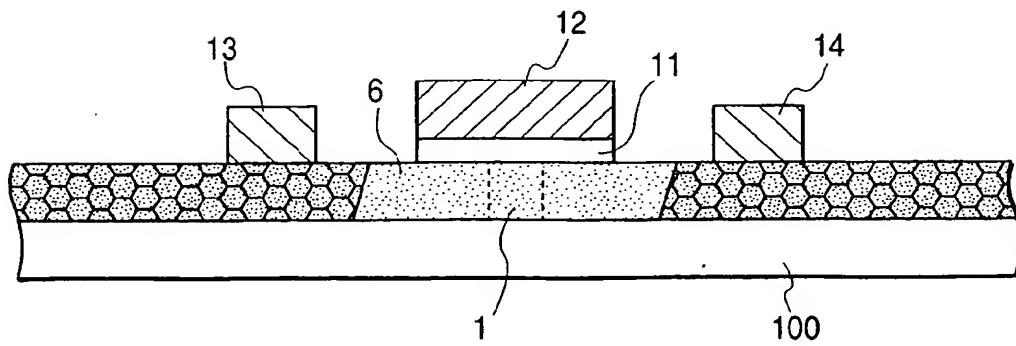


FIG. 14

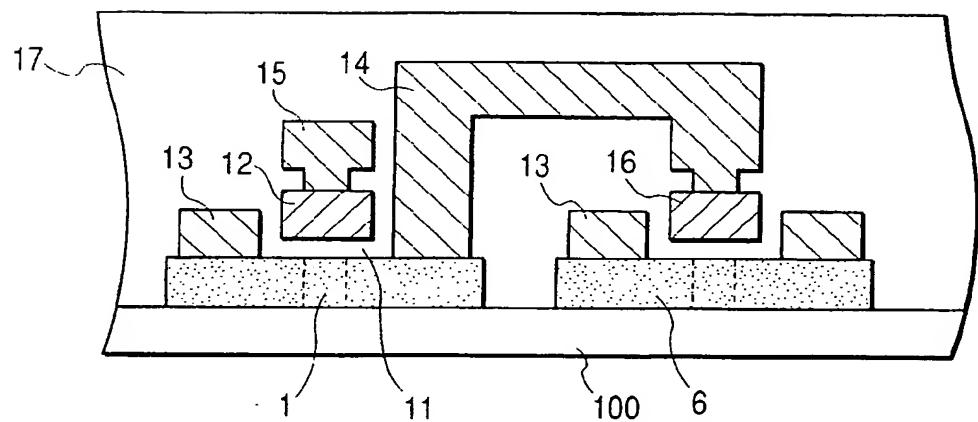
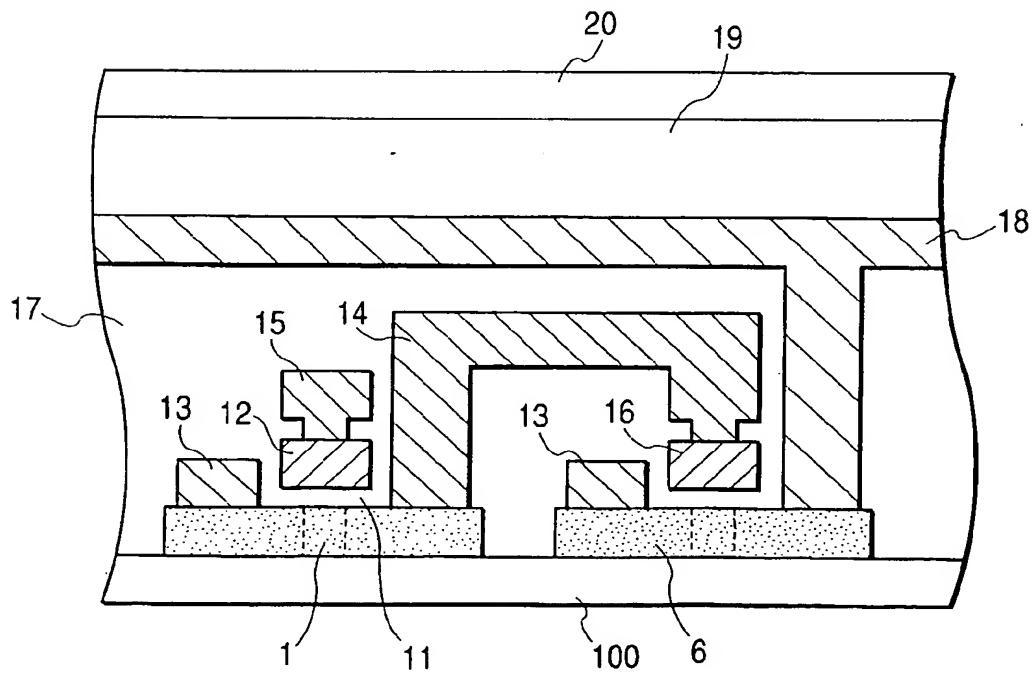


FIG. 15





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## EUROPEAN SEARCH REPORT

Application Number  
EP 02 01 2126

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			-/-
<p>The present search report has been drawn up for all claims</p>			
Place of search	Date of completion of the search	Examiner	
MUNICH	10 September 2002	Brisson, O	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
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EP 02 01 2126

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			-/-
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search		Examiner
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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
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			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
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10-09-2002

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